

Fig. 1

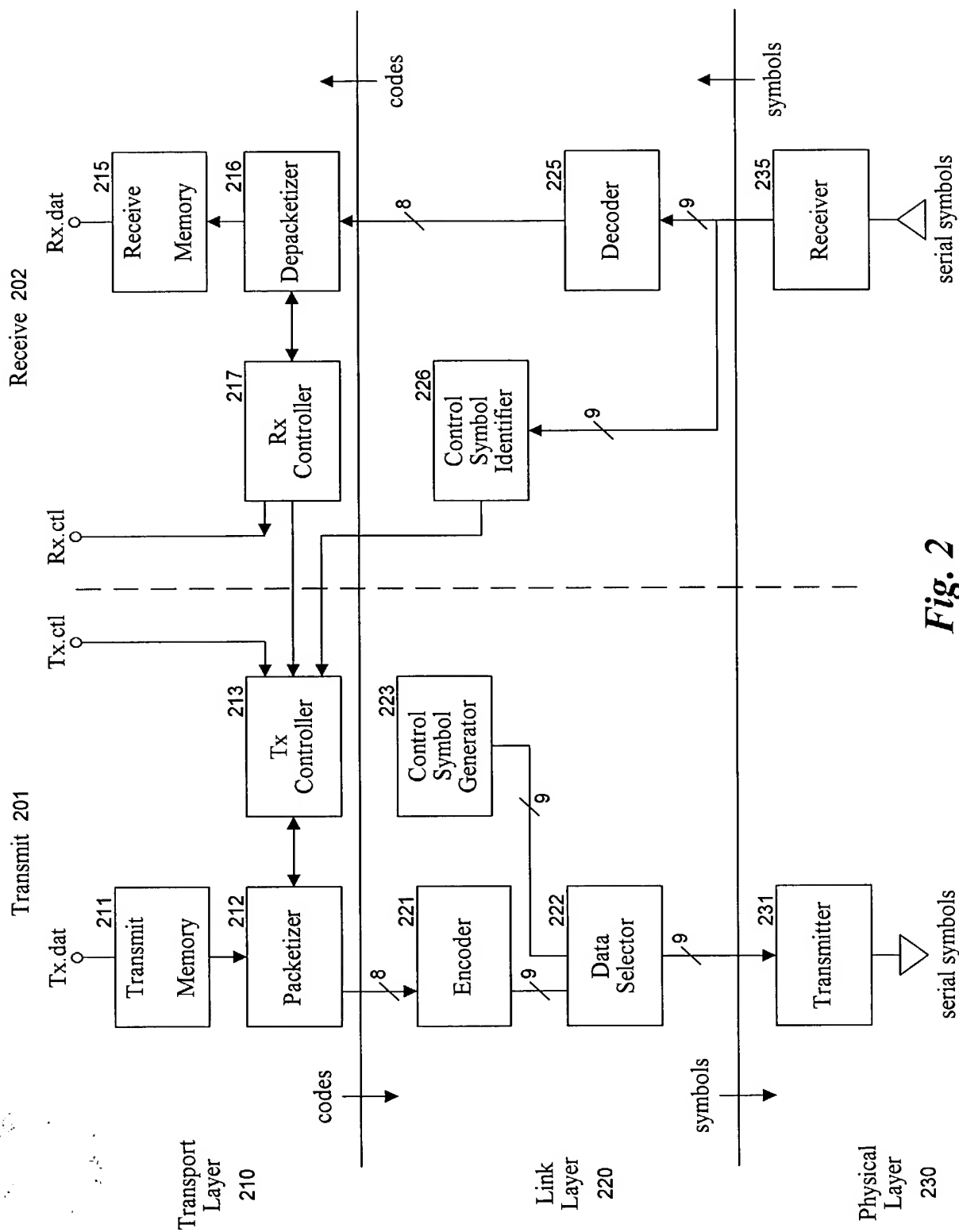


Fig. 2

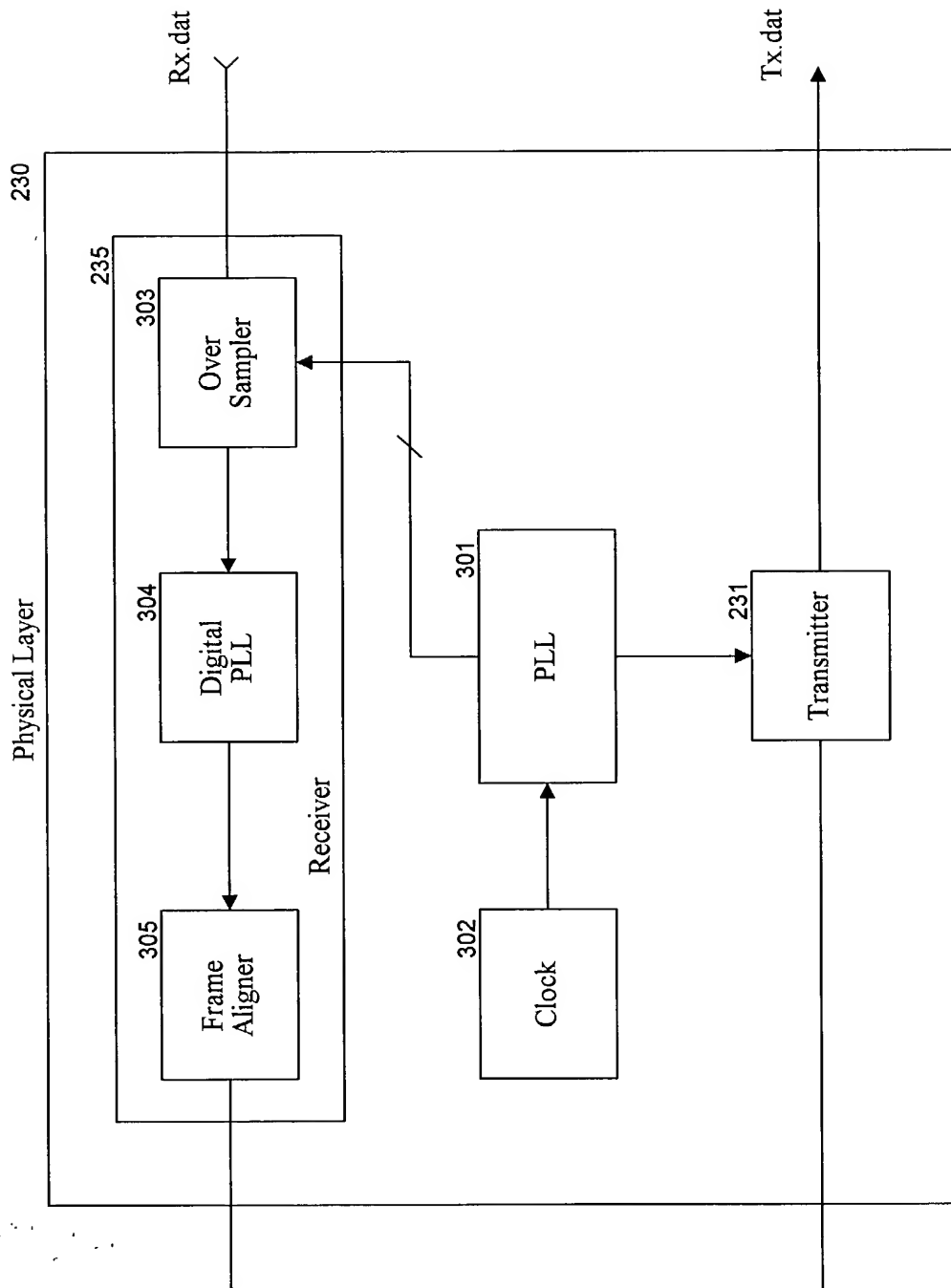


Fig. 4

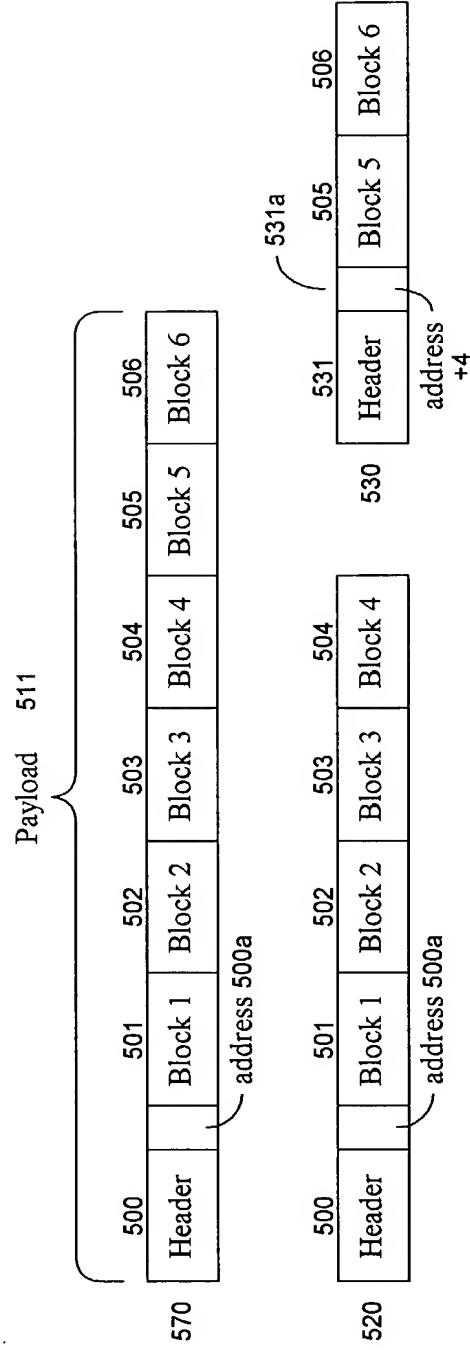
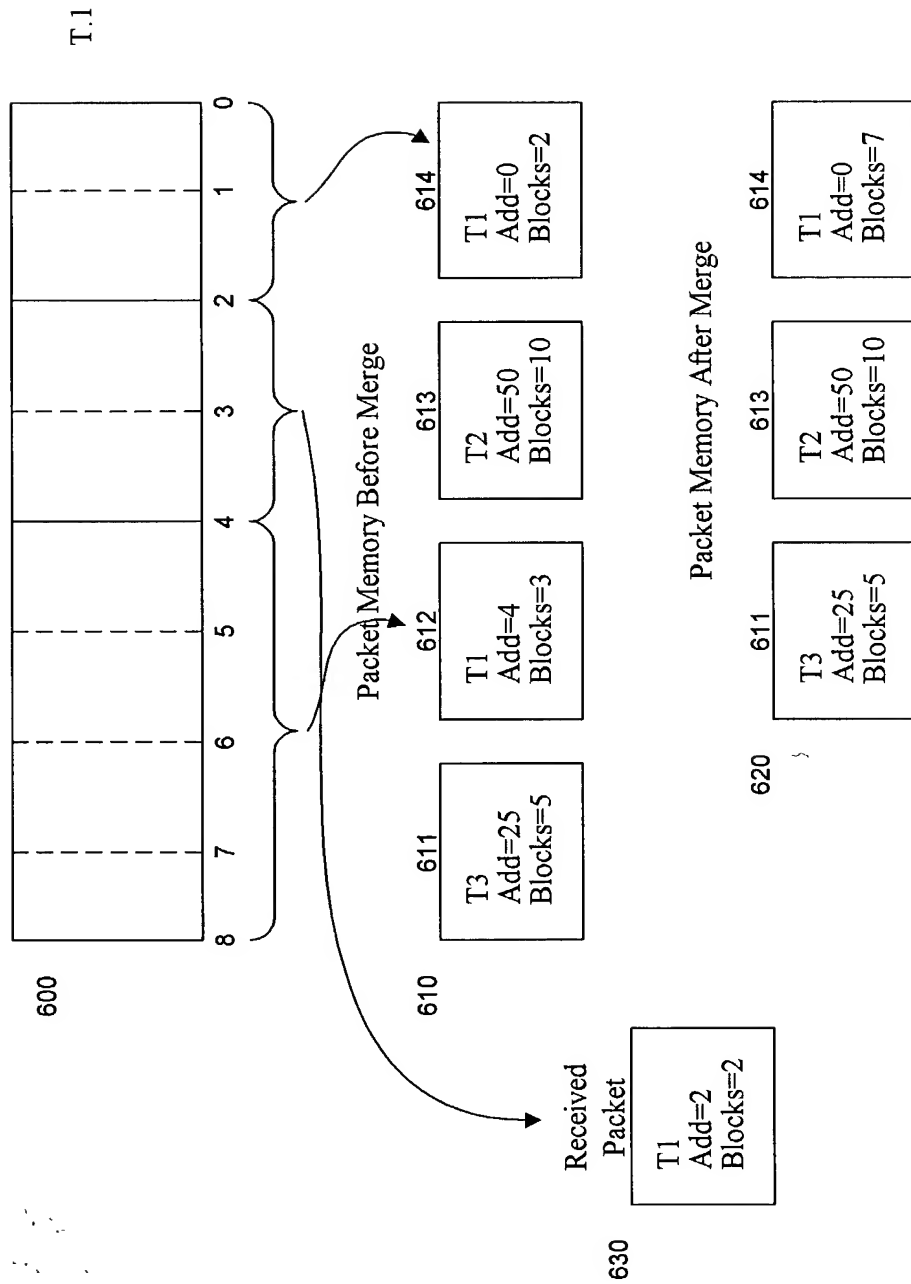
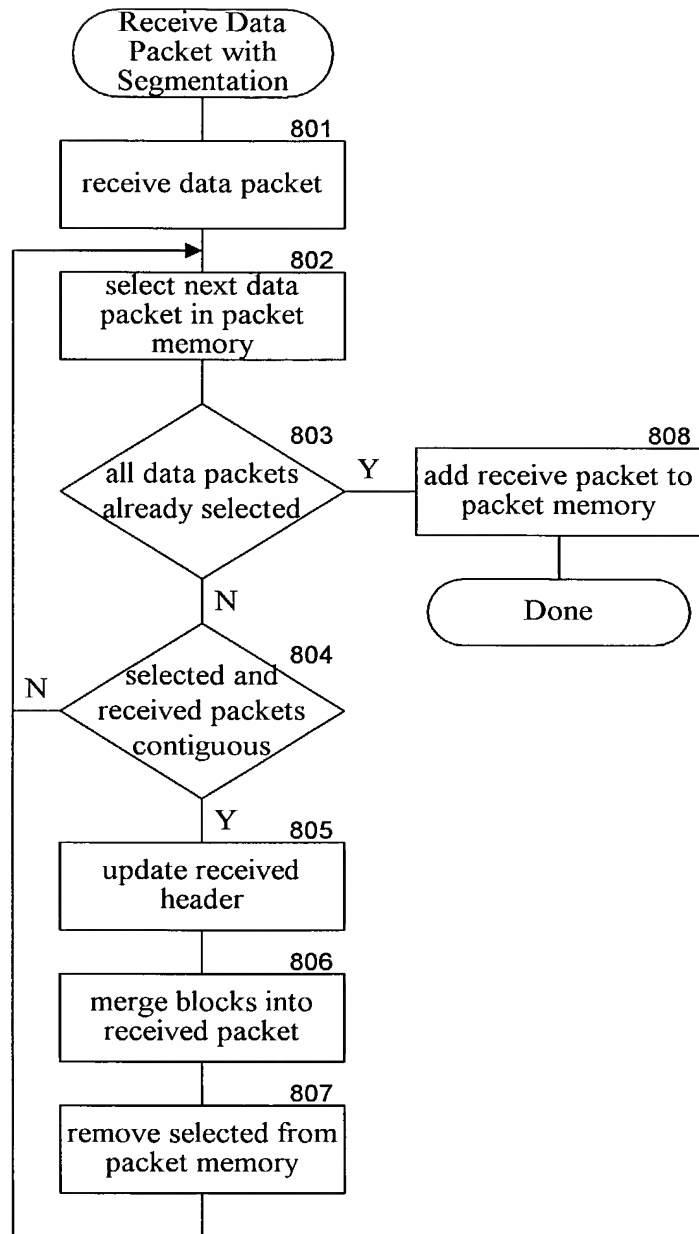
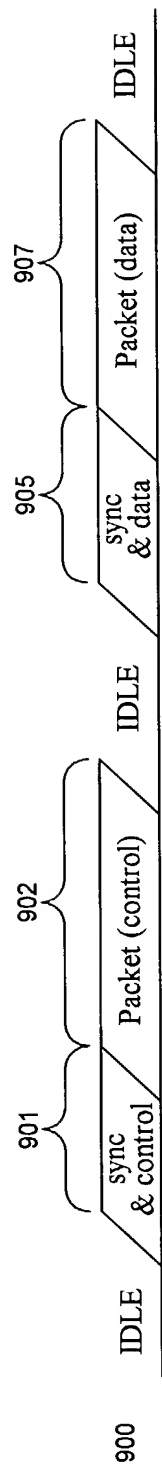


Fig. 5



**Fig. 8**



sync & packet type

Fig. 9A

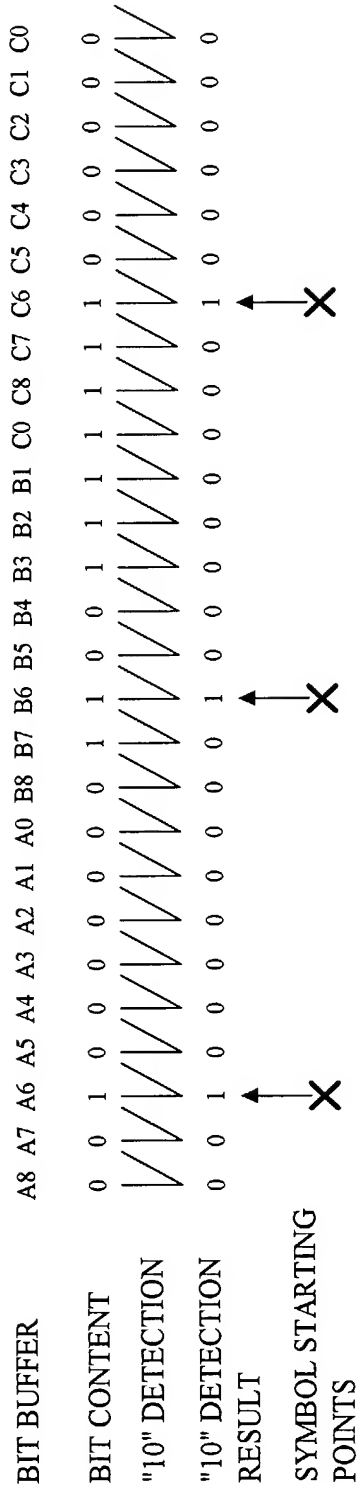


Fig. 9B

10045625 . 05 0002

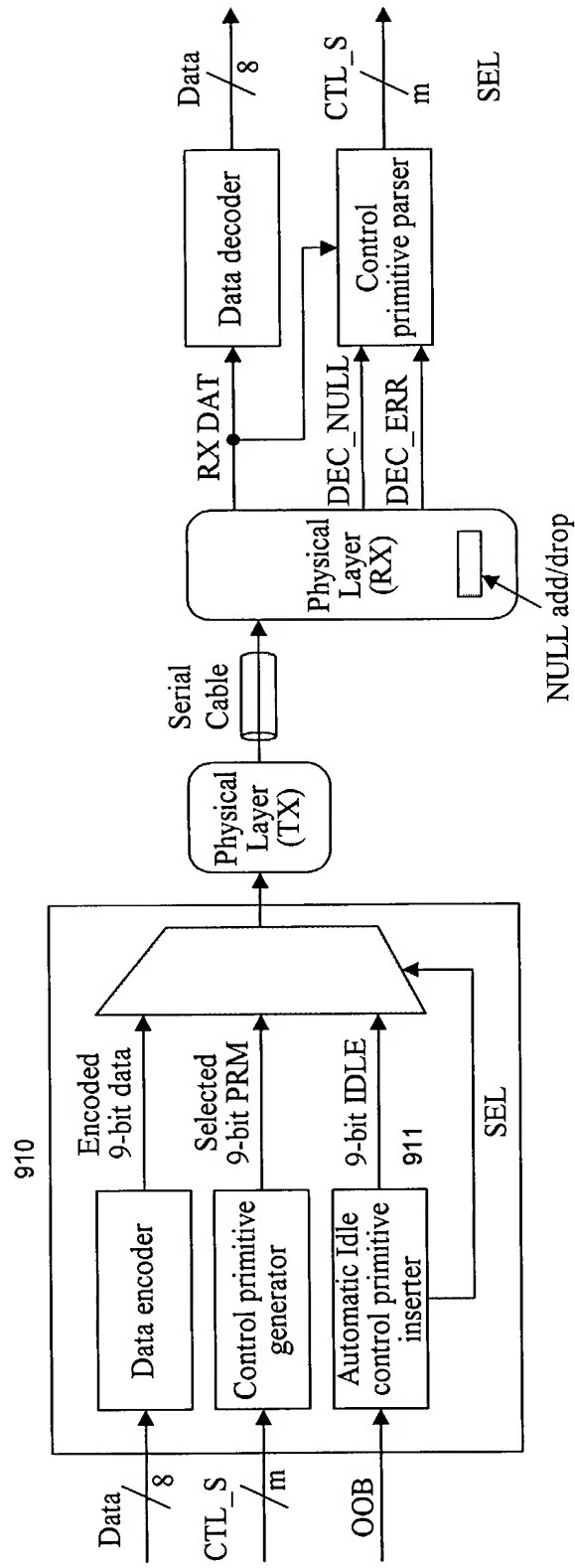


Fig. 9C

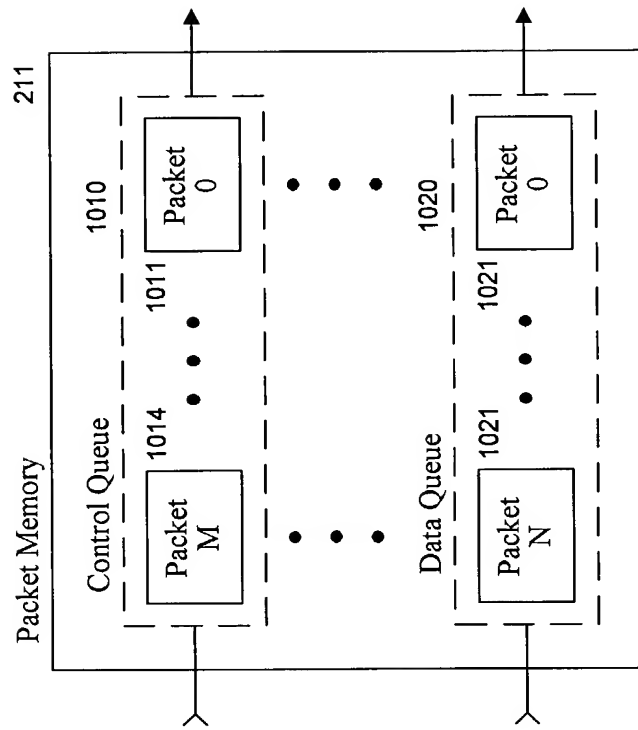


Fig. 10

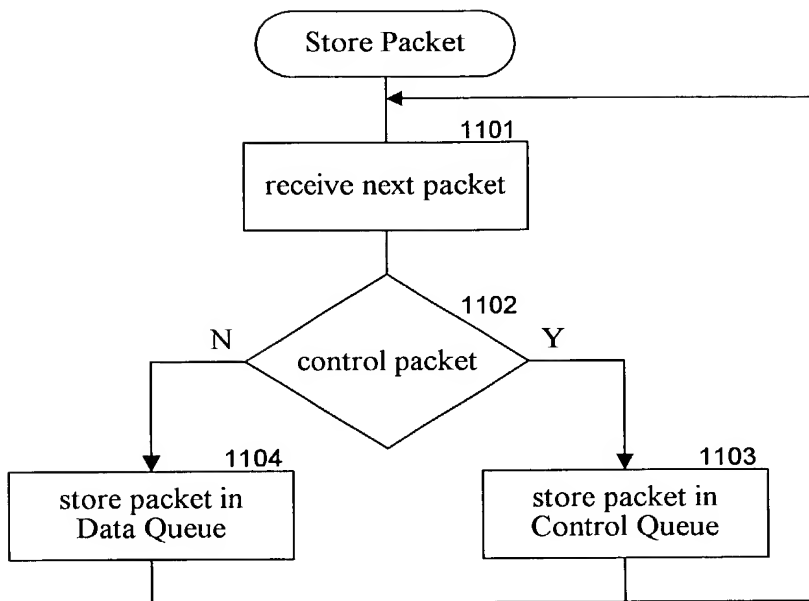


Fig. 11

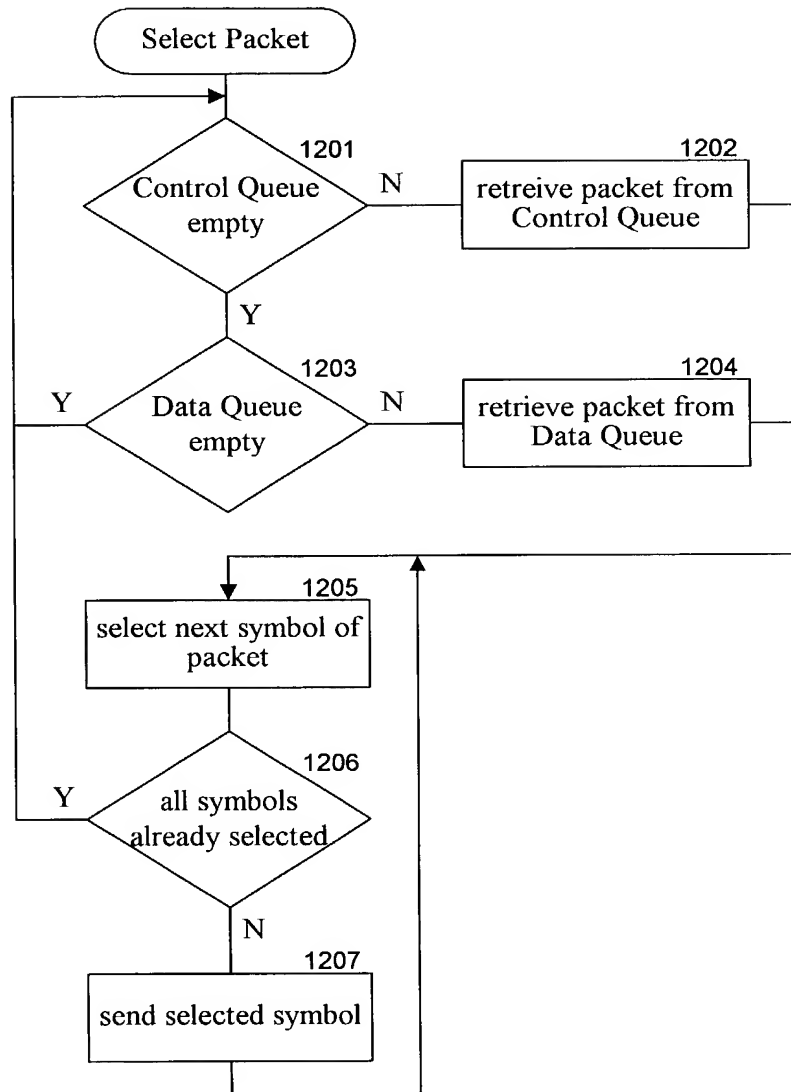
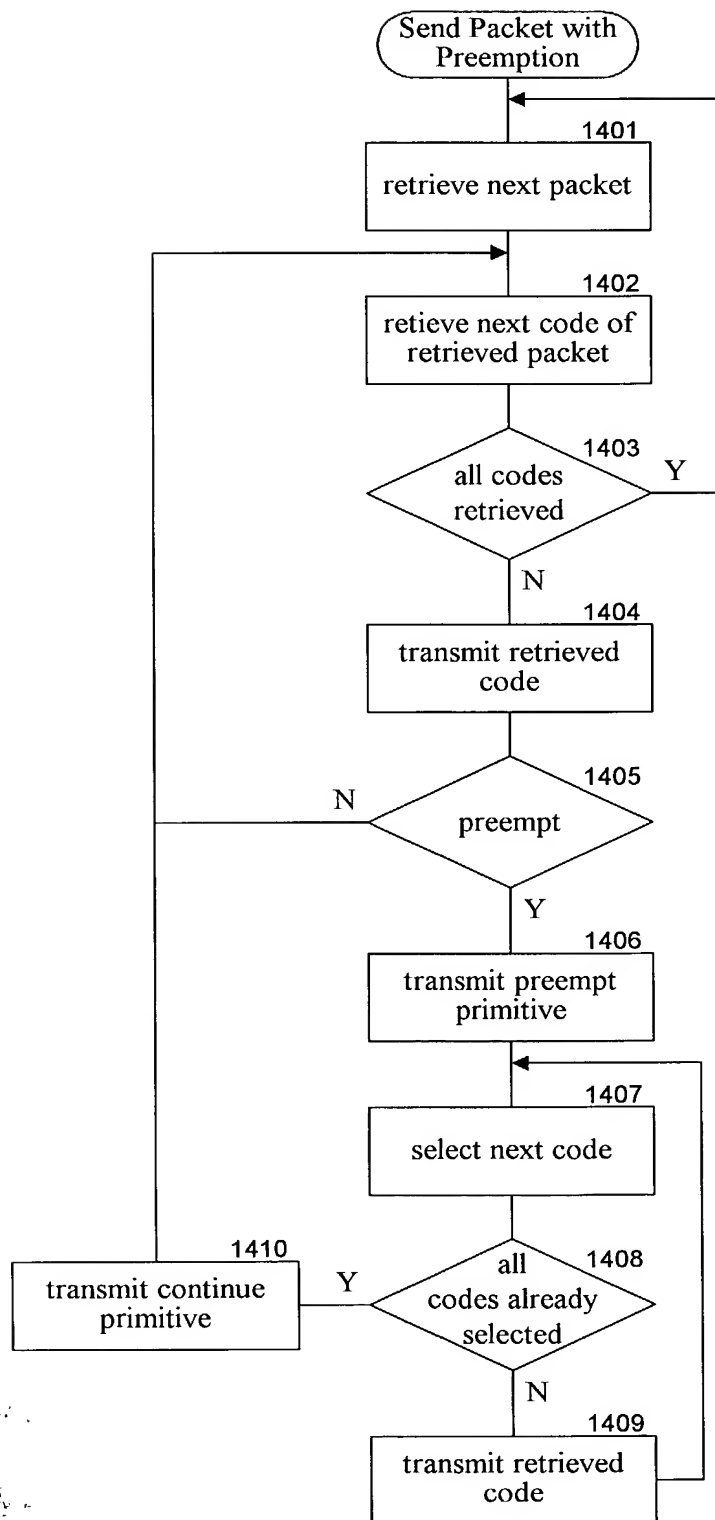


Fig. 12

**Fig. 14**

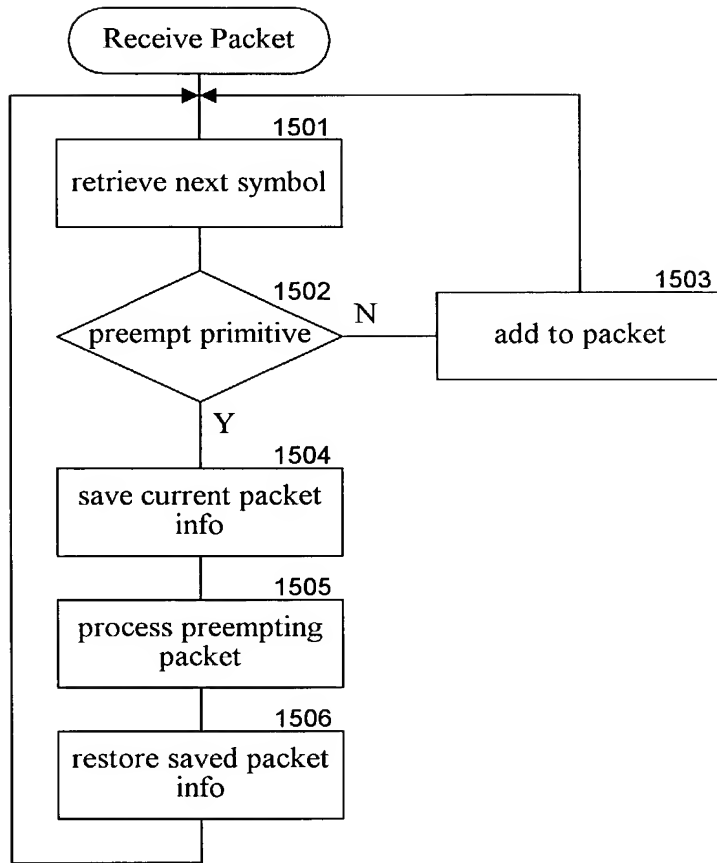


Fig. 15

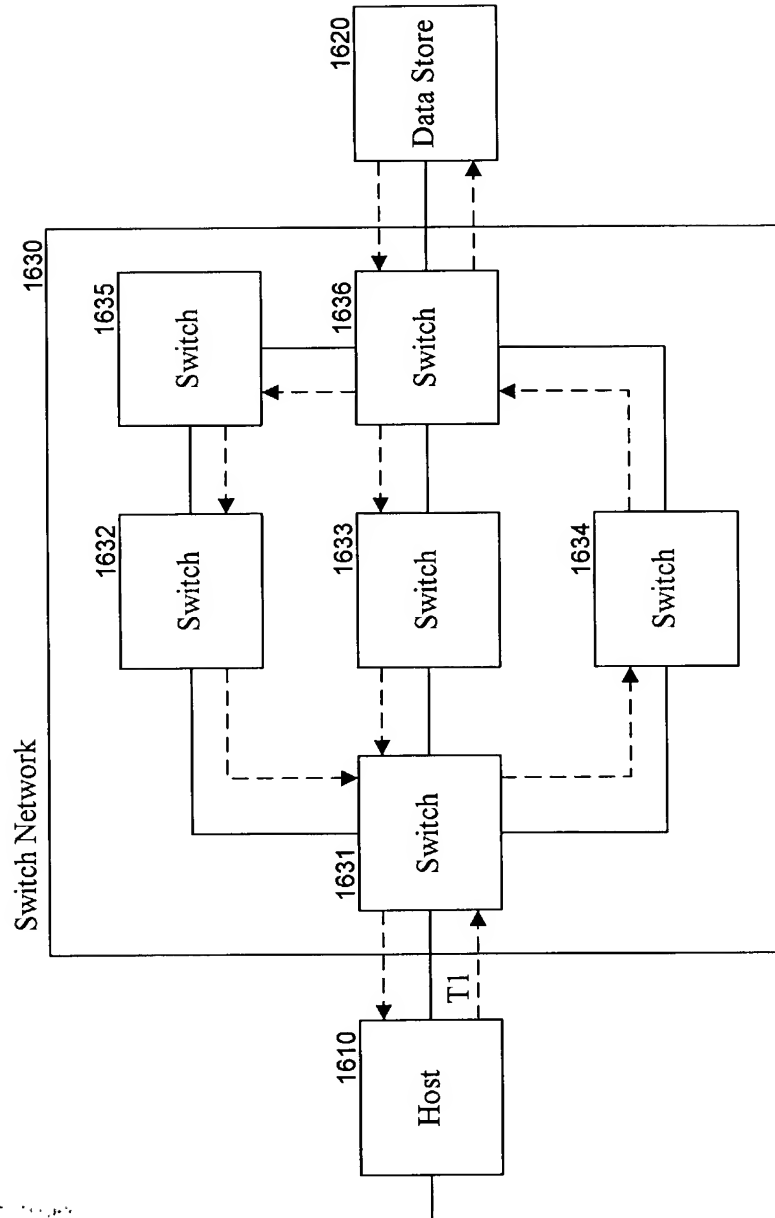


Fig. 16

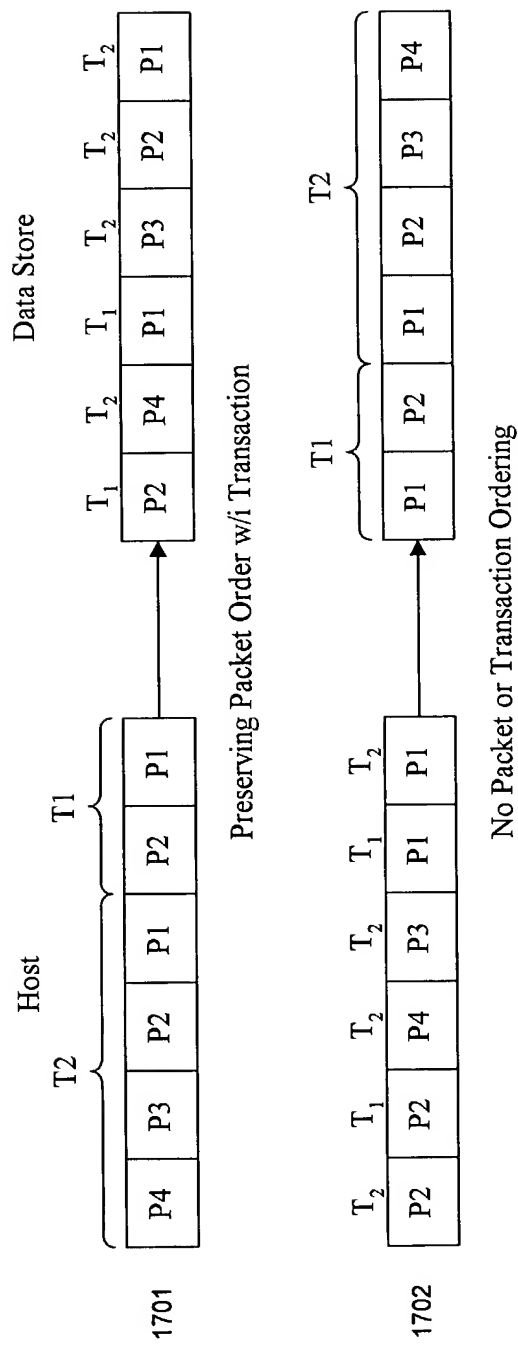
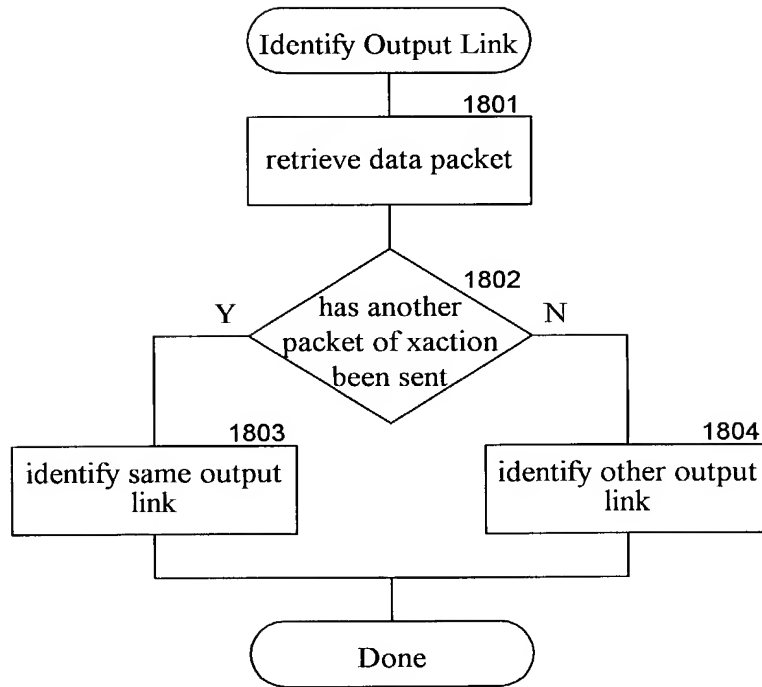


Fig. 17

**Fig. 18**

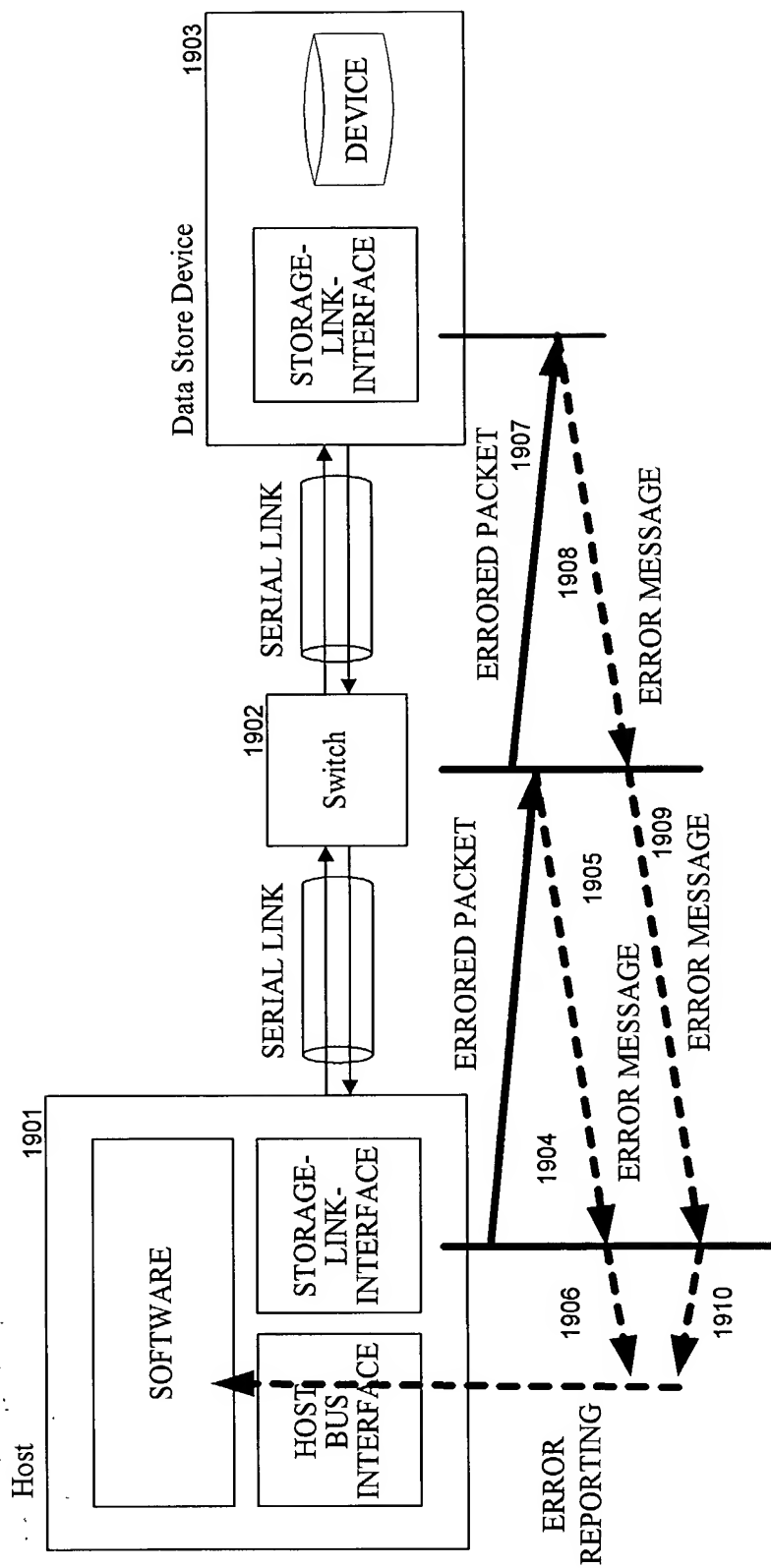


Fig. 19A

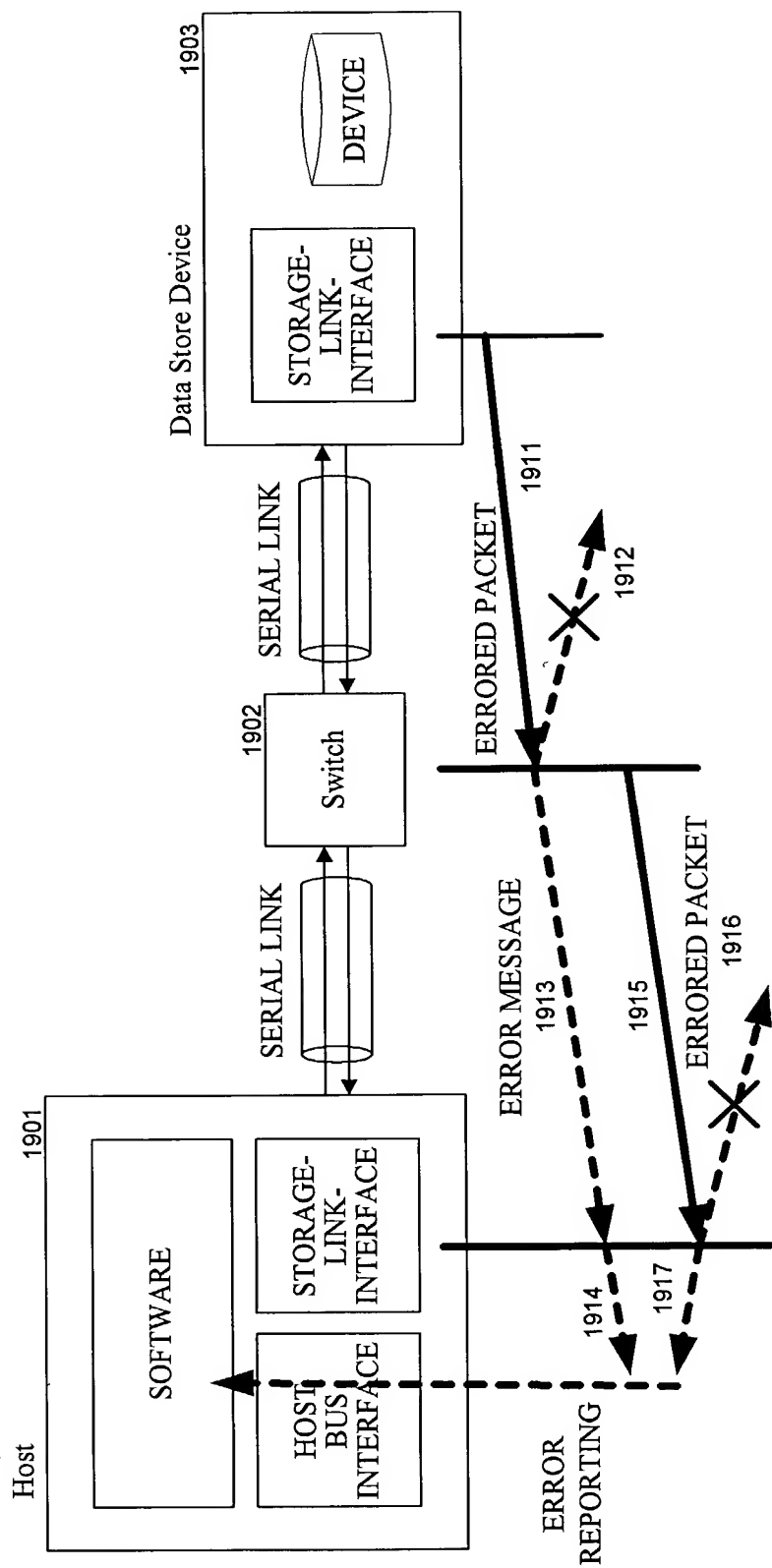


Fig. 19B

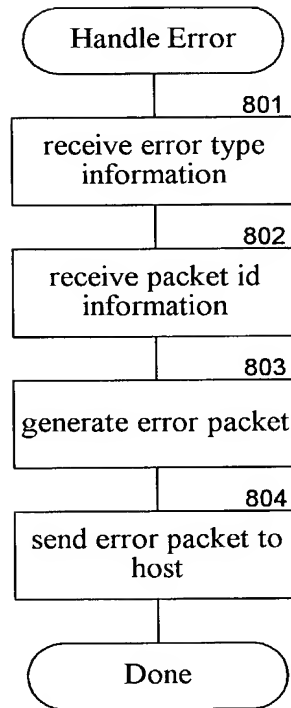


Fig. 19C

Fig. 20



Fig. 21A

10044433 = 050005

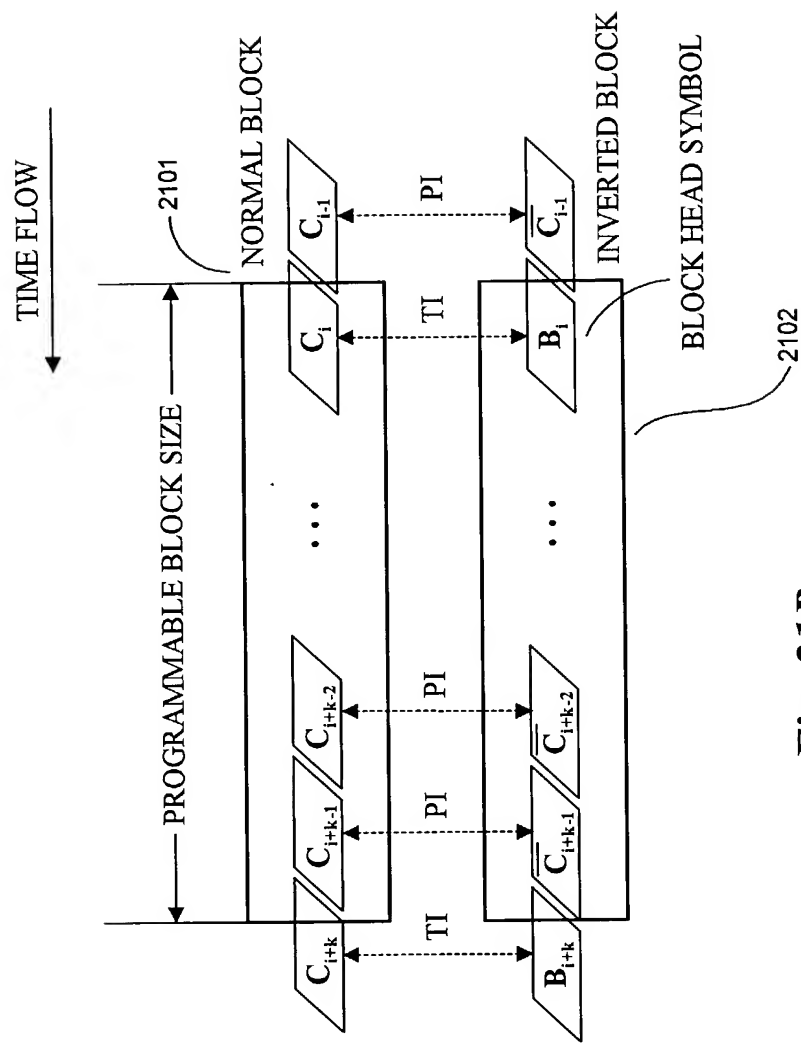


Fig. 21B

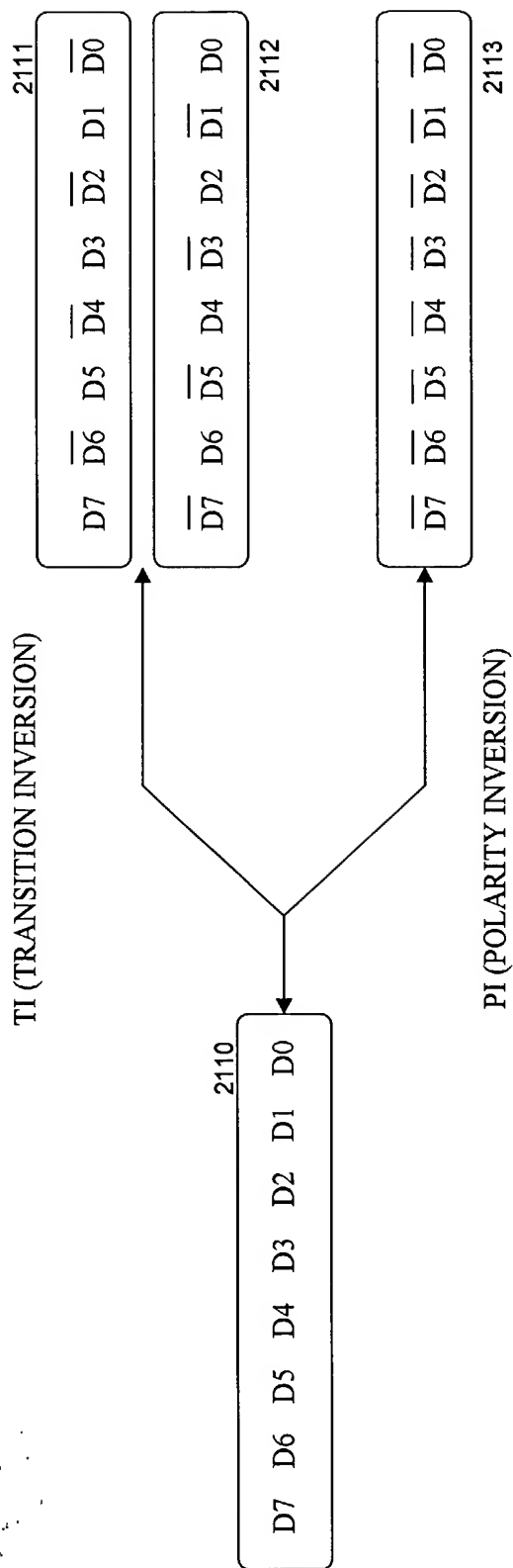


Fig. 21C

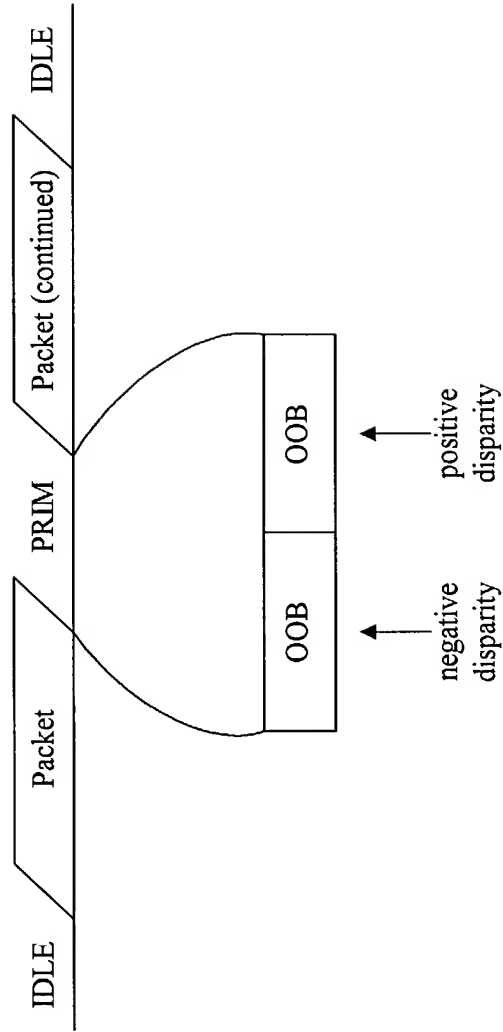
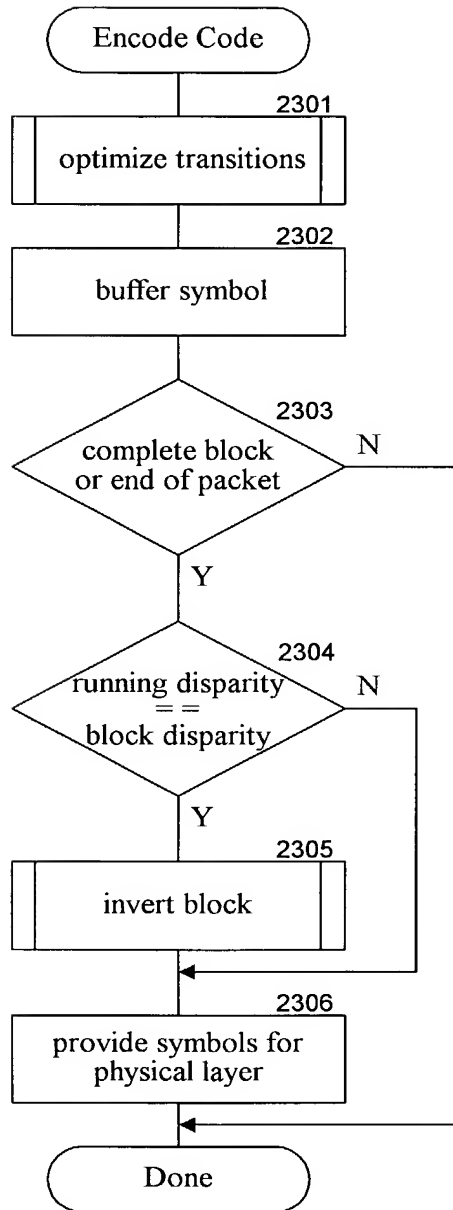


Fig. 22

**Fig. 23**

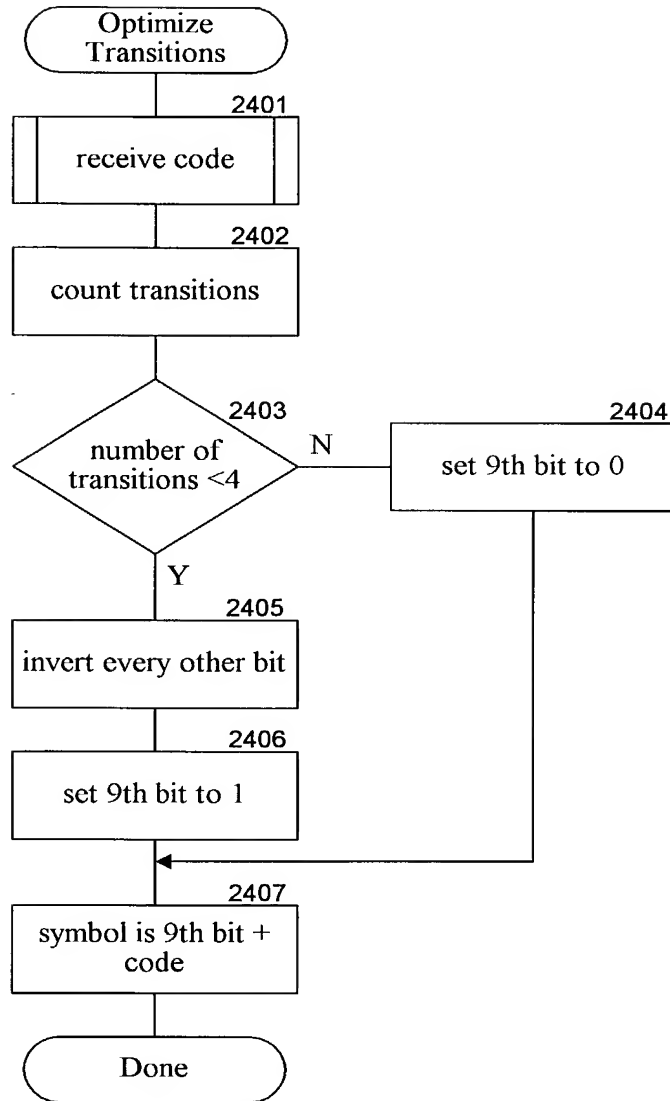


Fig. 24

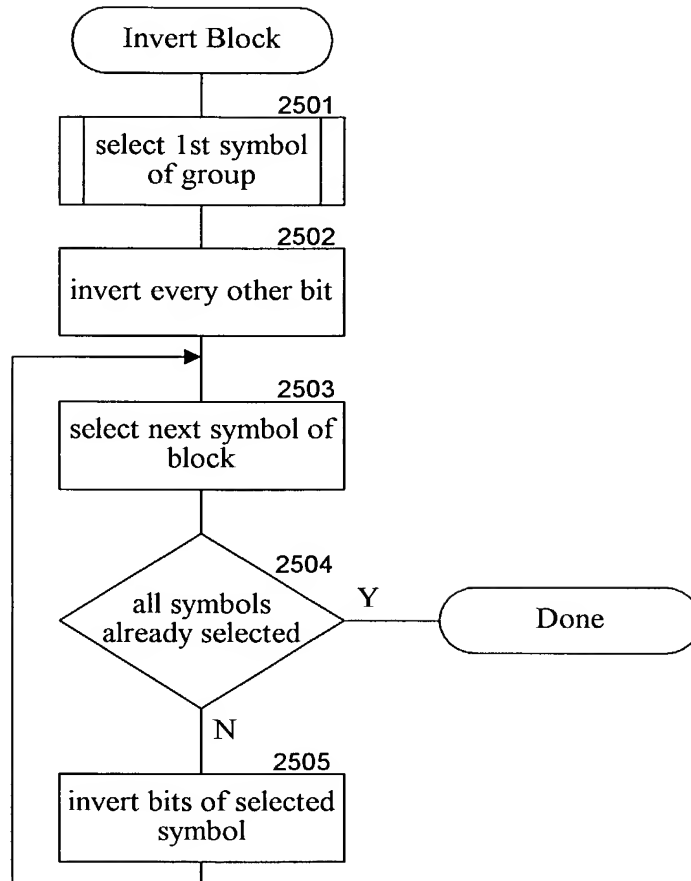


Fig. 25

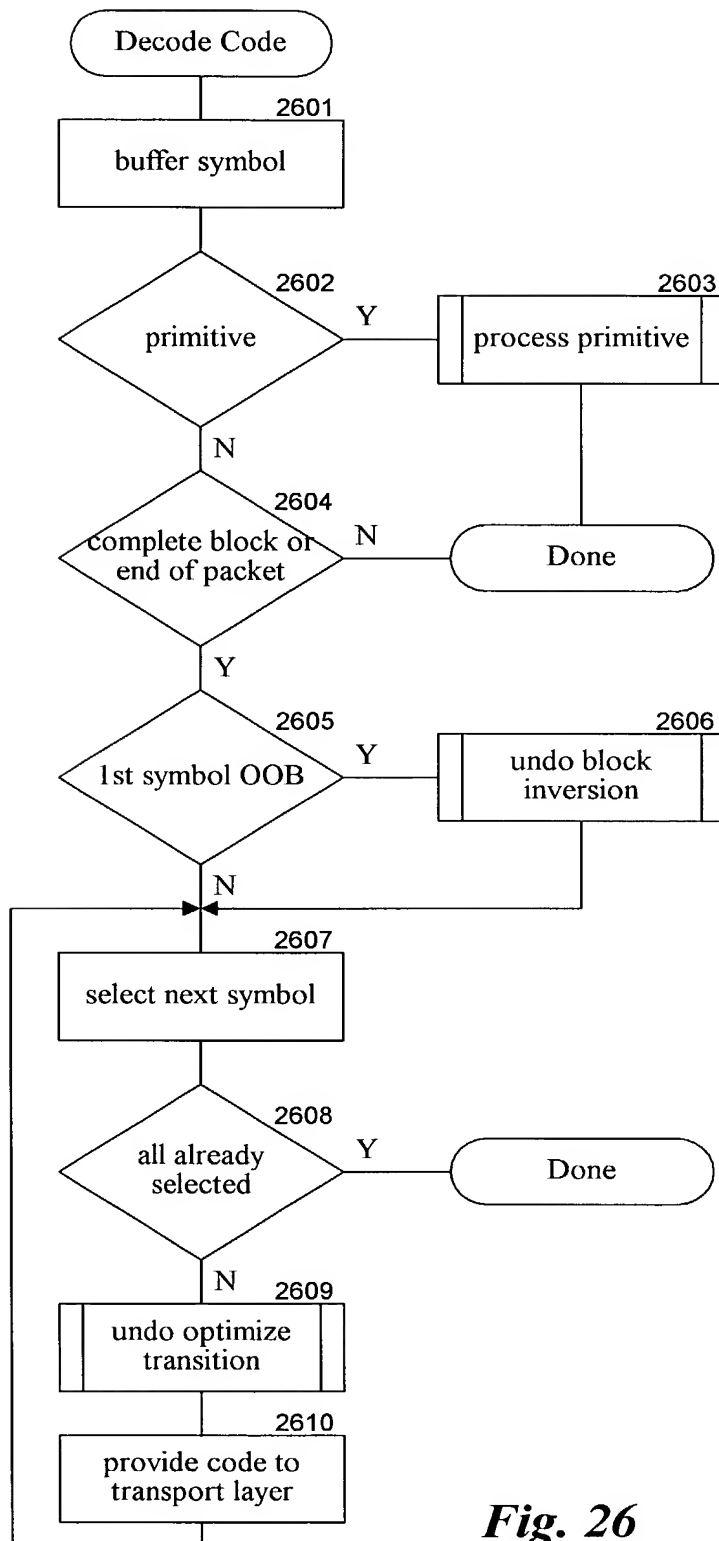


Fig. 26

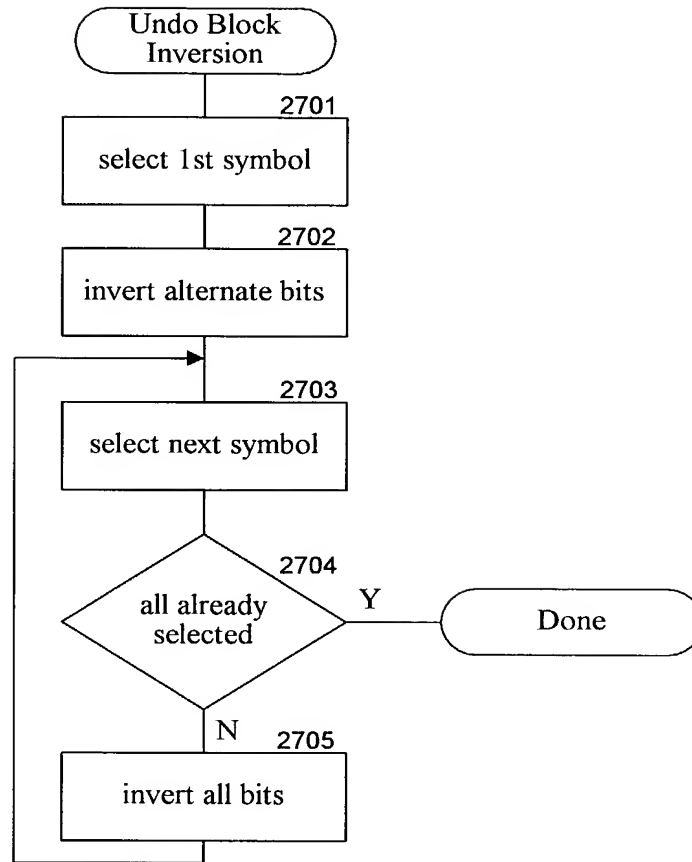


Fig. 27

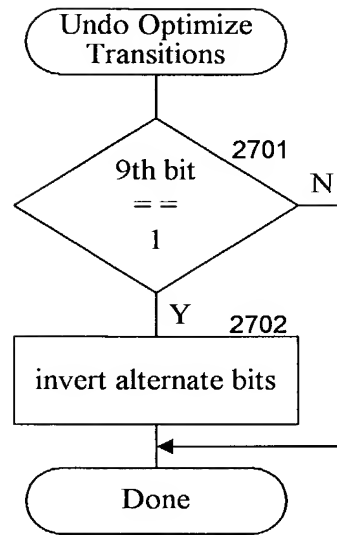
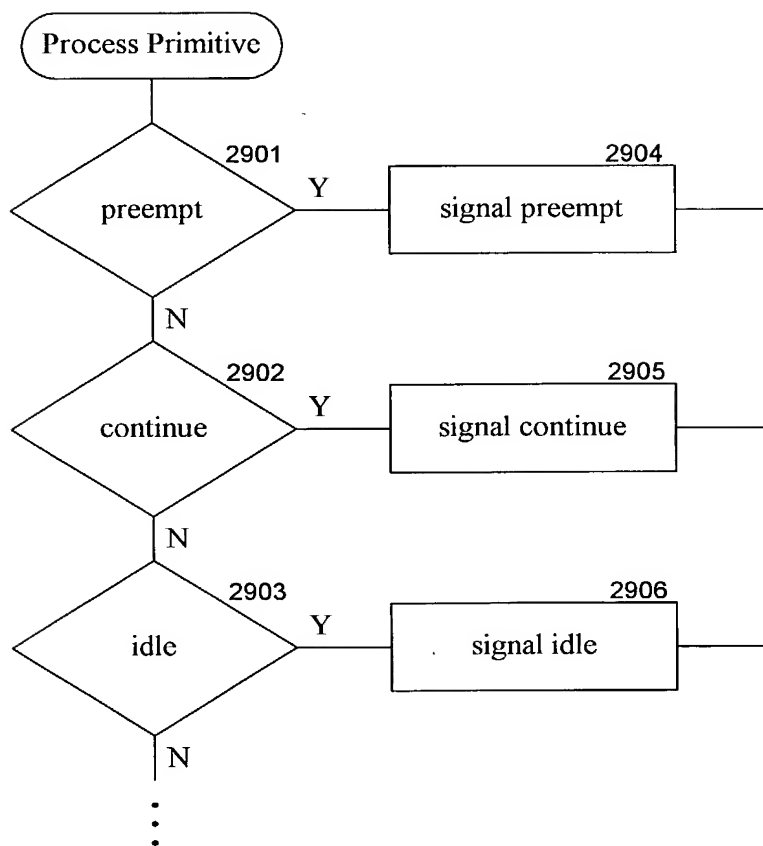


Fig. 28

**Fig. 29**

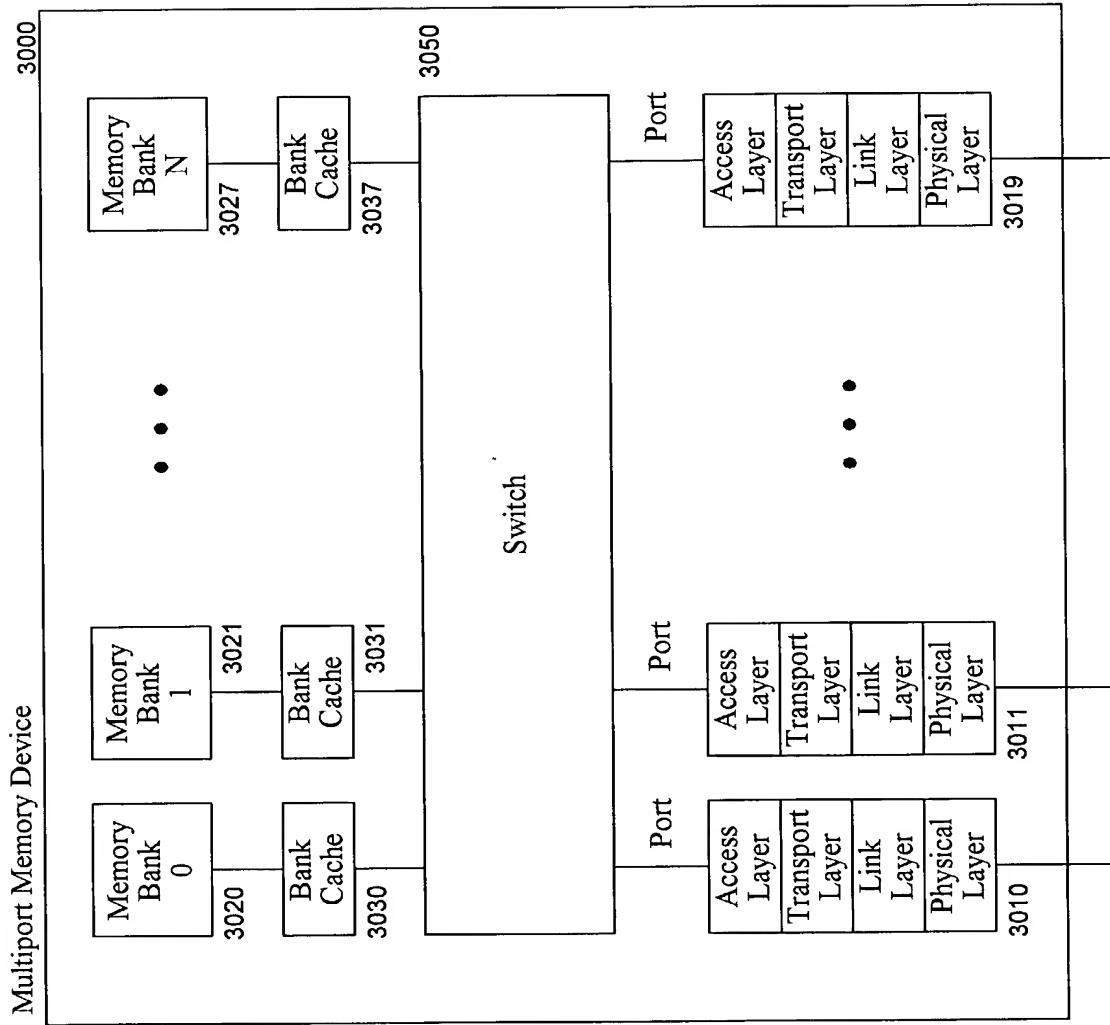


Fig. 30

FIG. 30

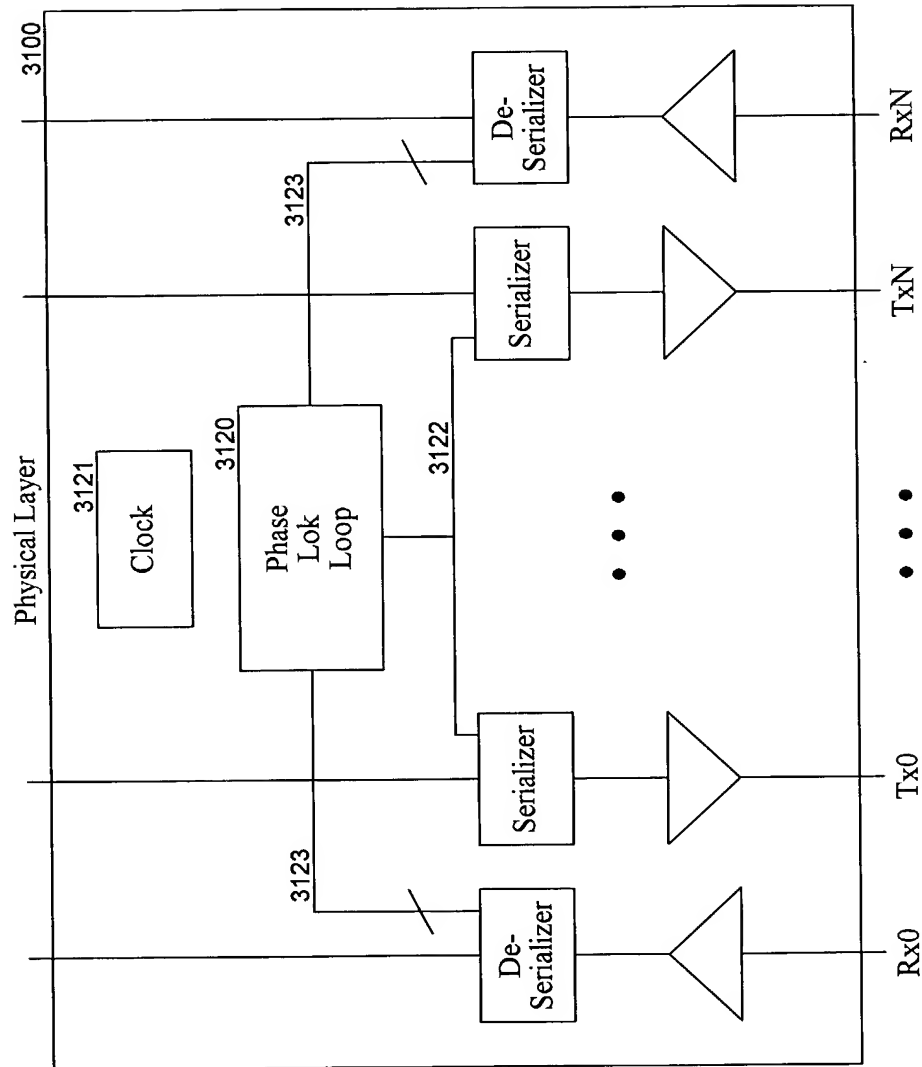


Fig. 31

Input Queue				Output Queue			
Port	R/W	Address	Data	Valid	Port	Data	
3	R	1000		1	3	11...0	
4	W	4000	10....1	0			
3	W	1000	111...0	0			
3	R	2000		1	3	101...1	
	⋮				⋮		

Fig. 32

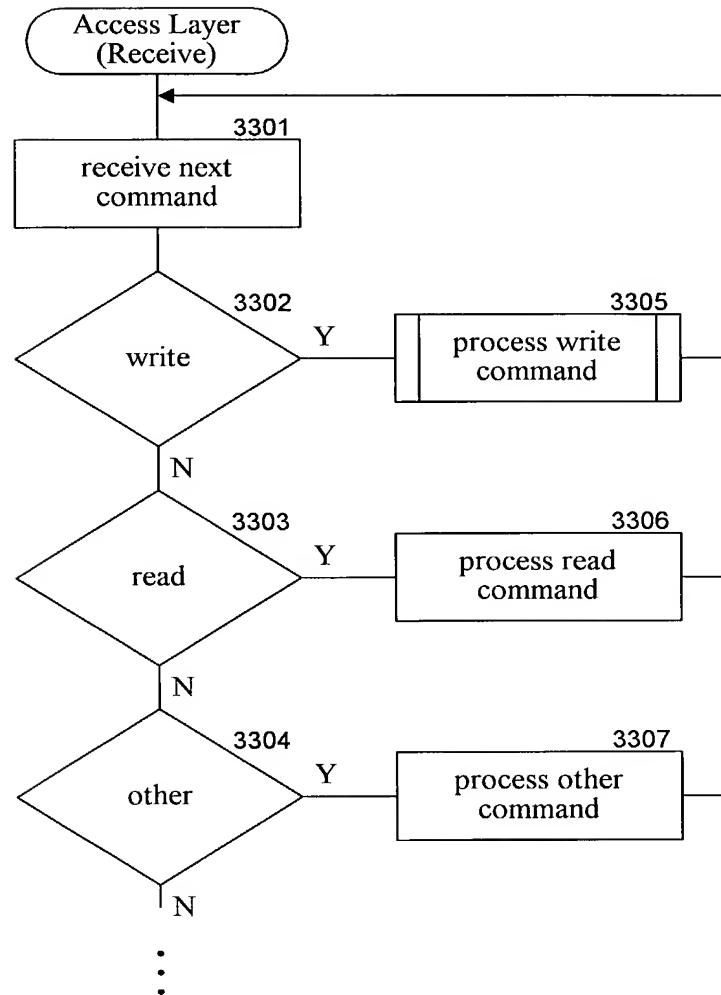


Fig. 33

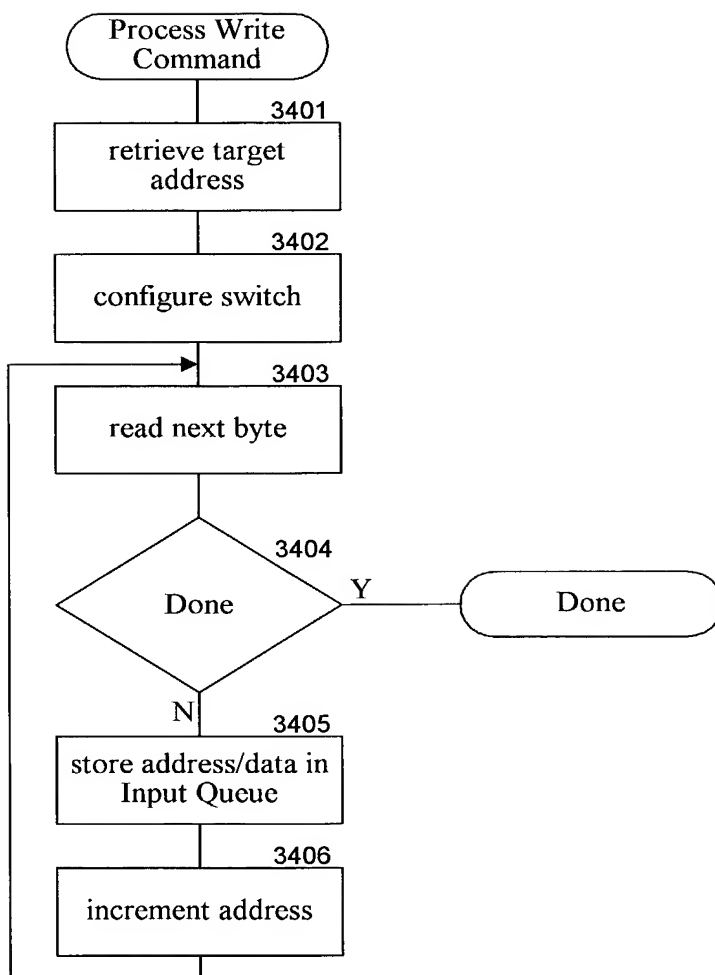
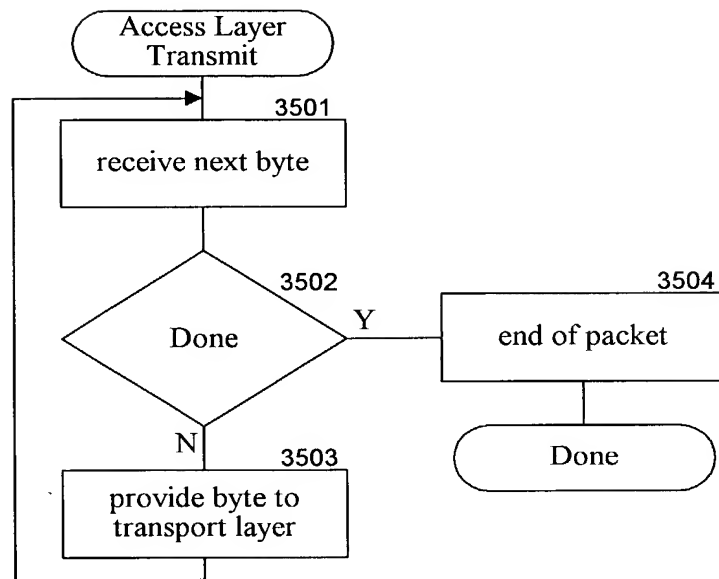


Fig. 34

**Fig. 35**

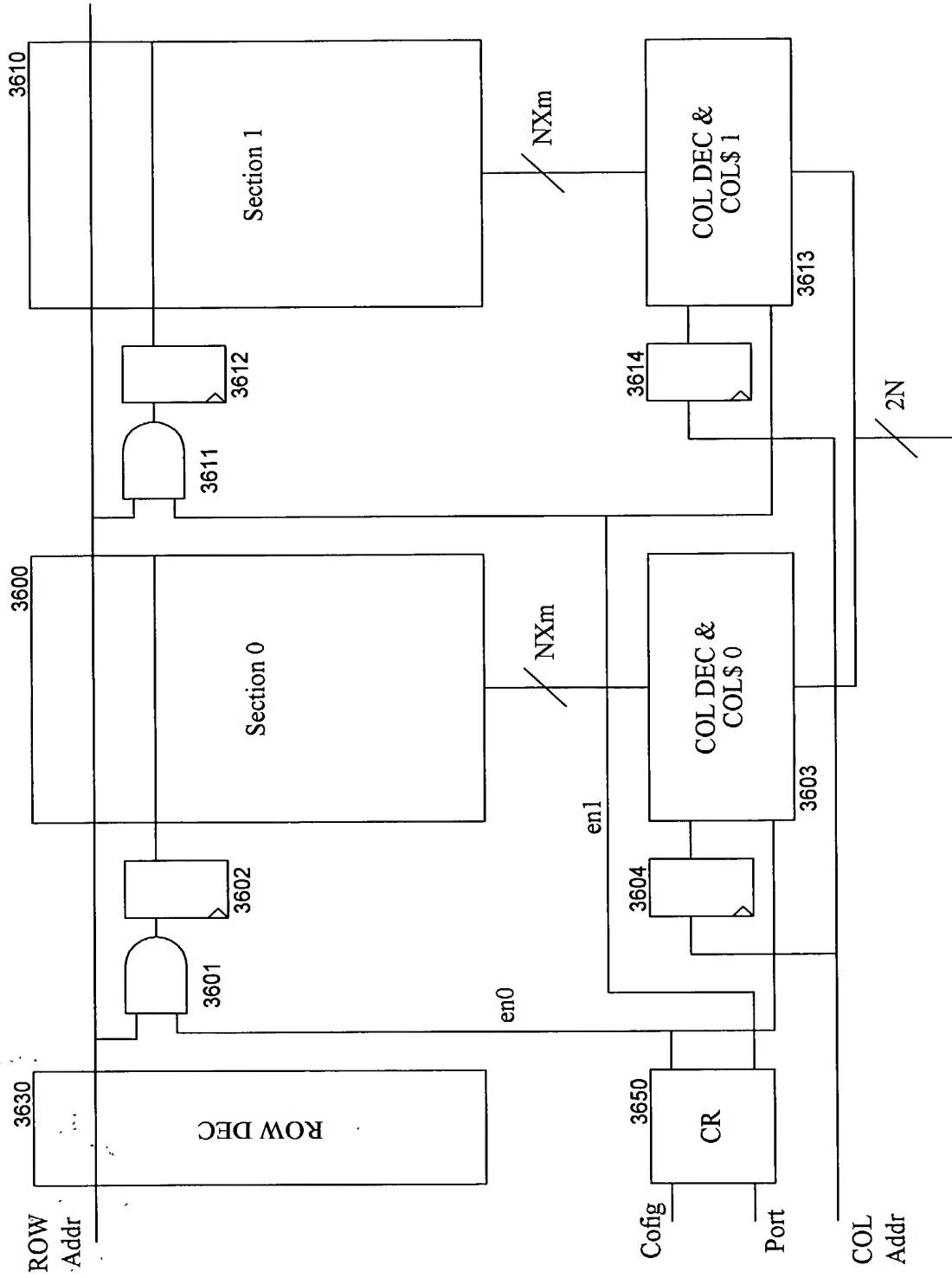
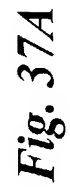


Fig. 36



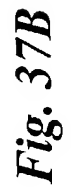


Fig. 37B

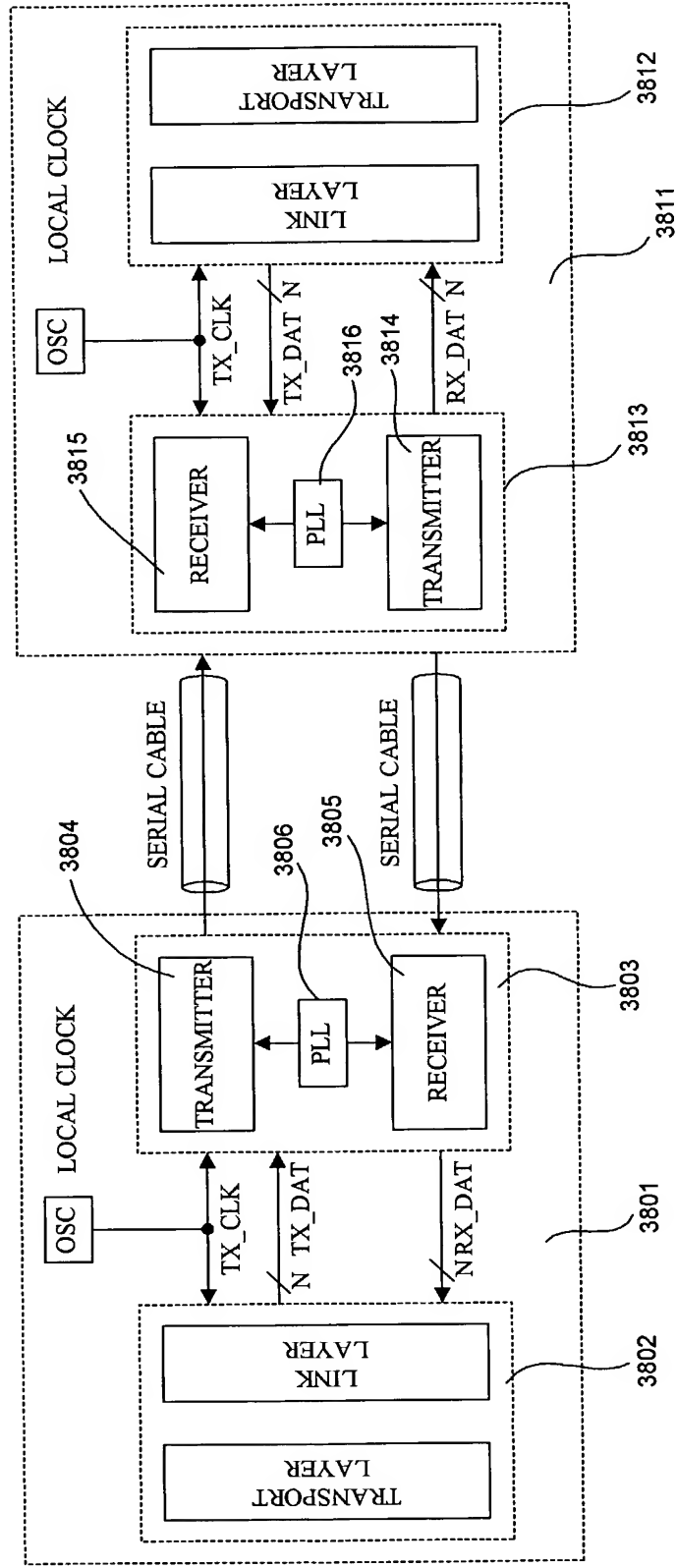


Fig. 38A

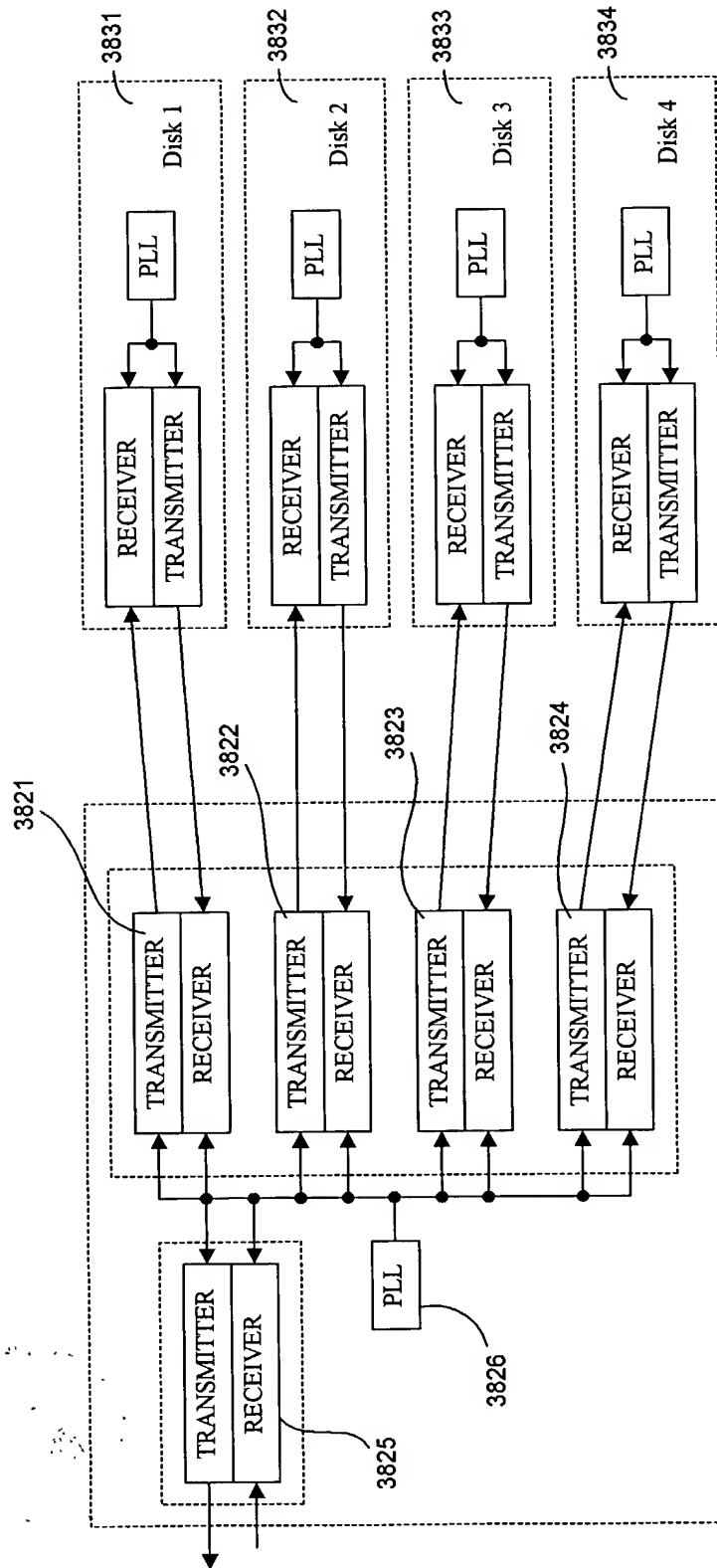
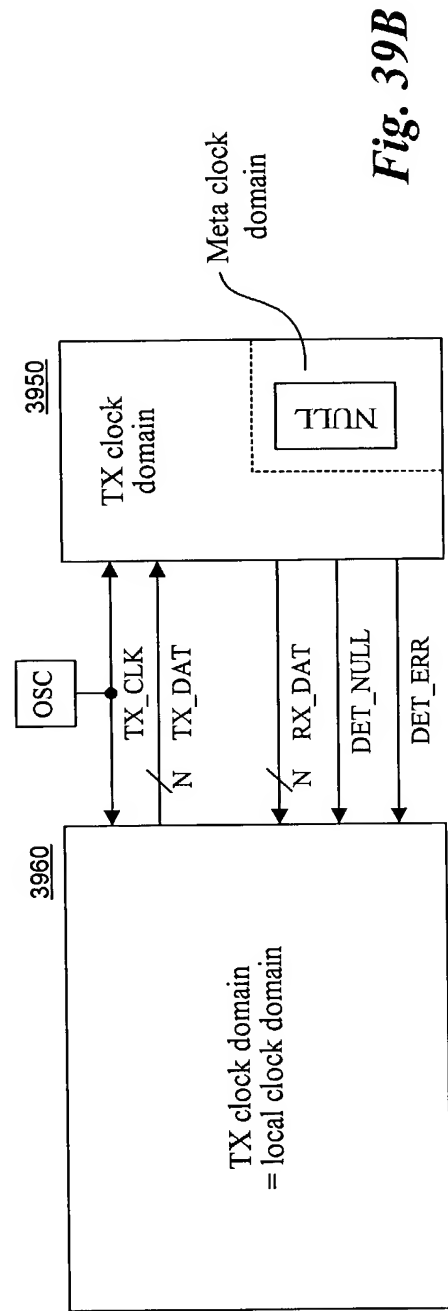
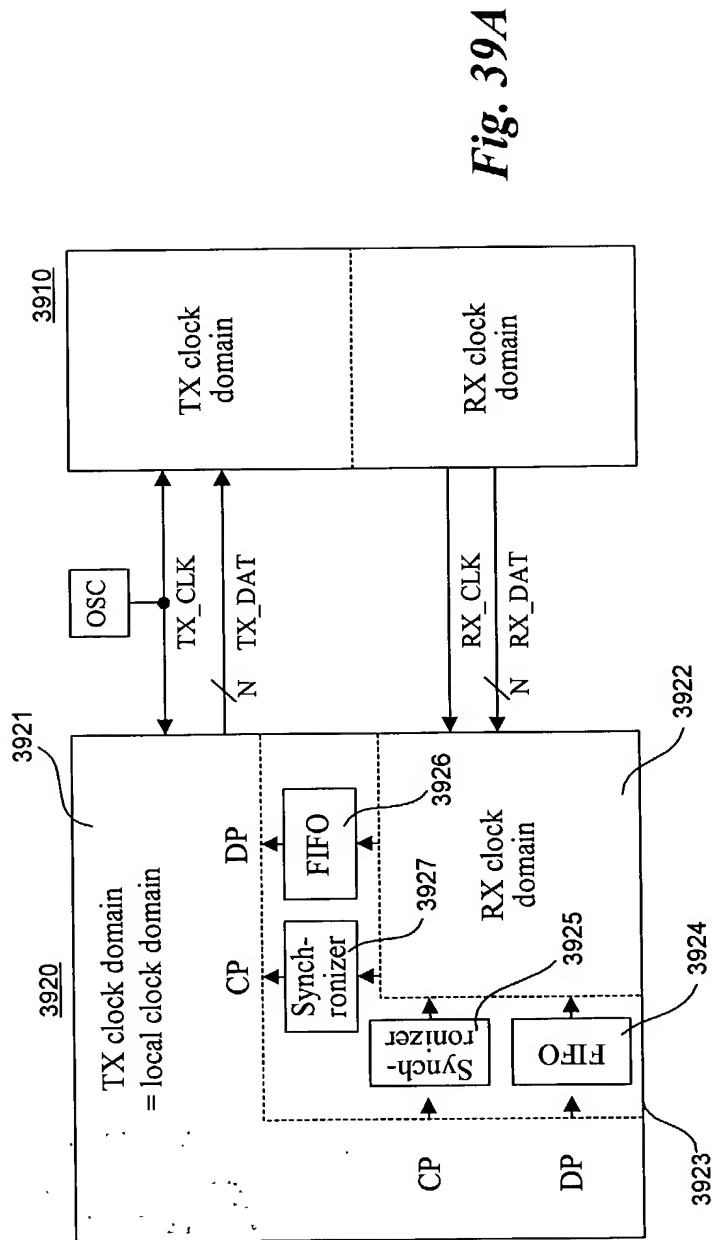
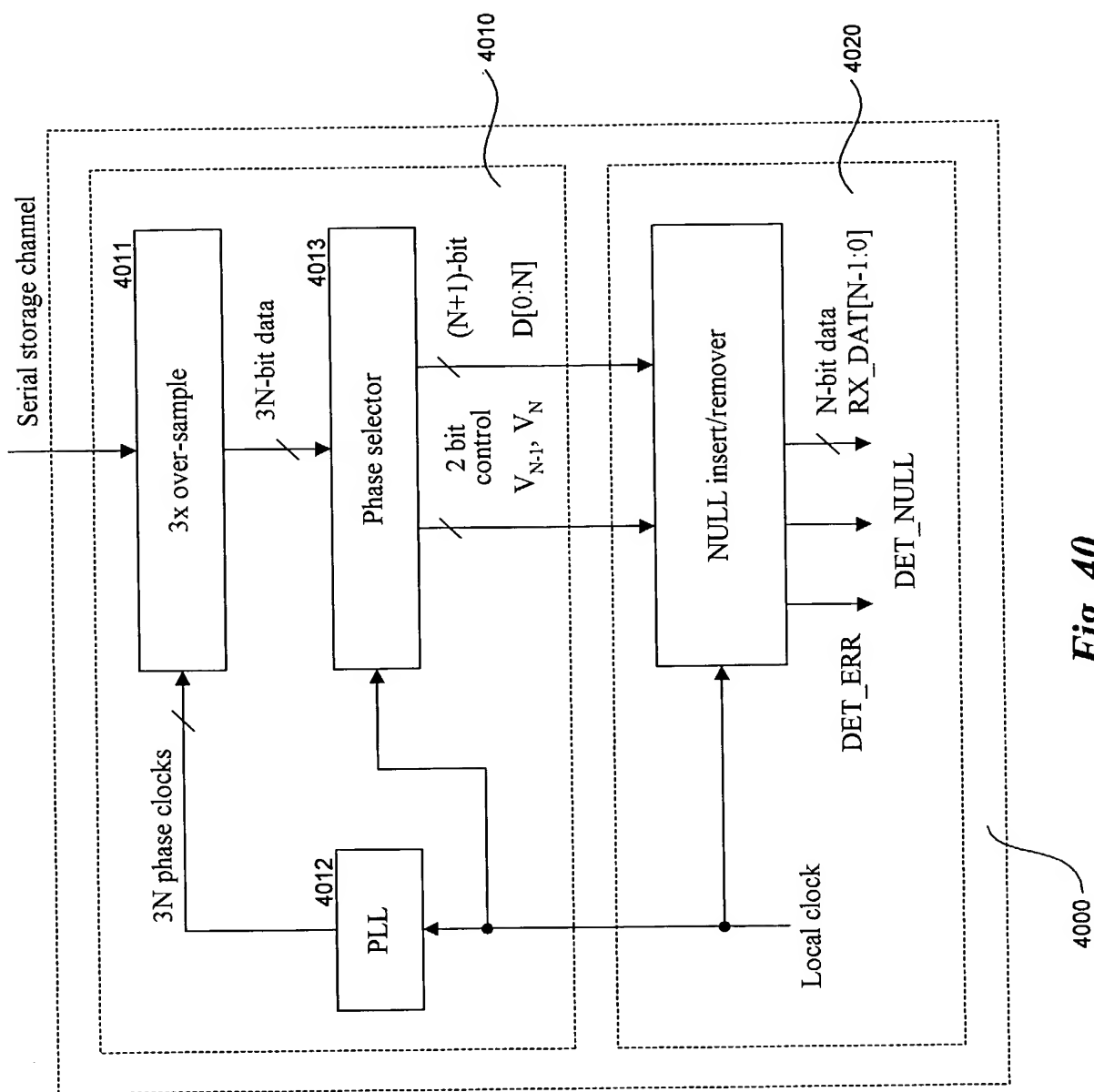


Fig. 38B





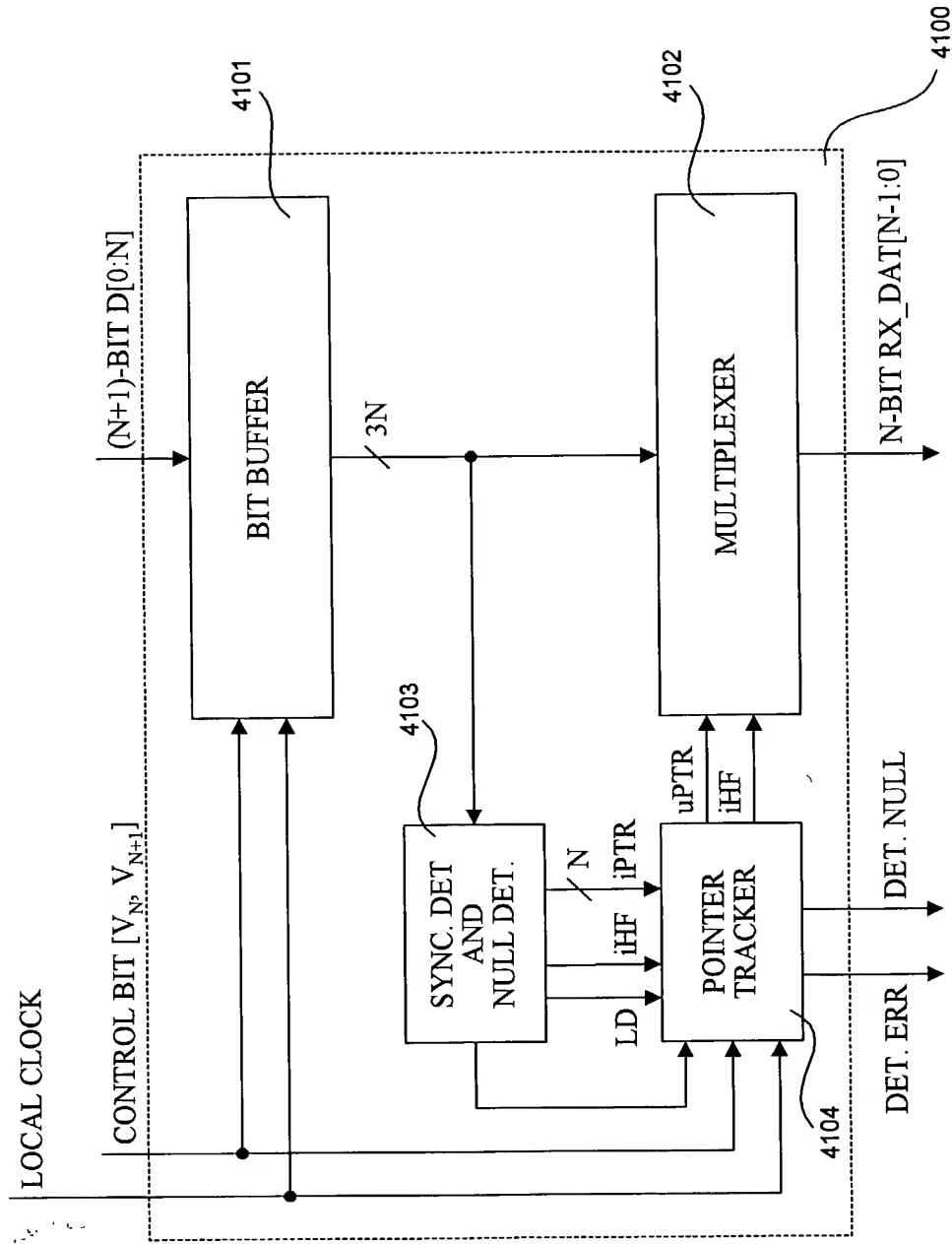


Fig. 41

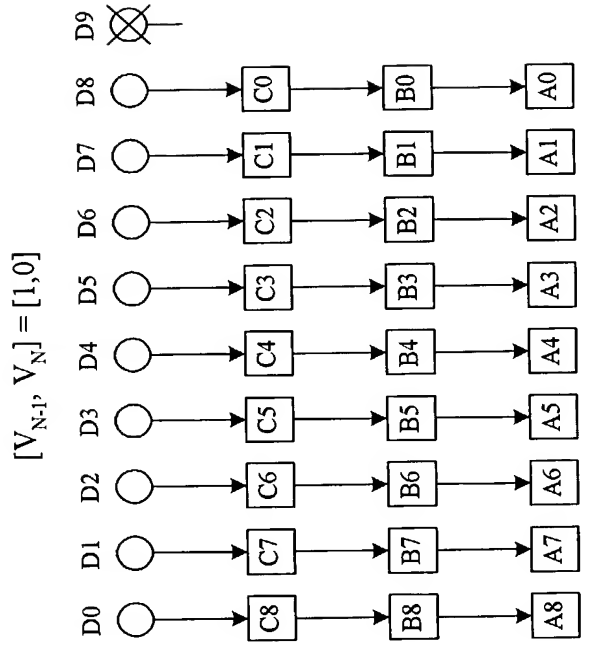
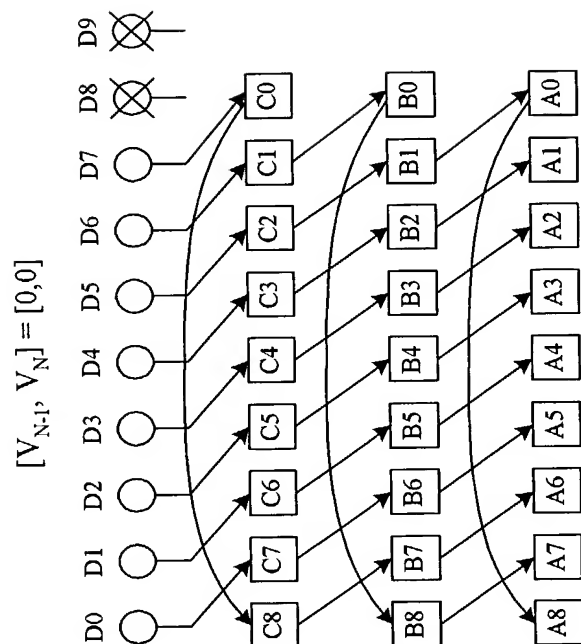
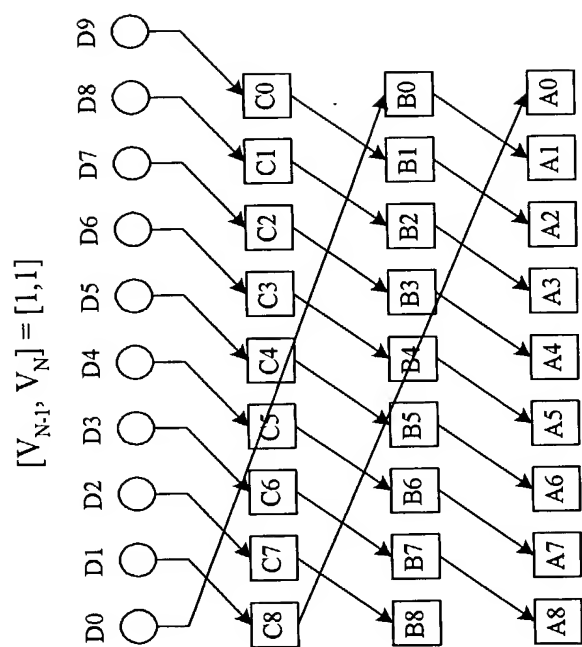


Fig. 42A

[illegible]



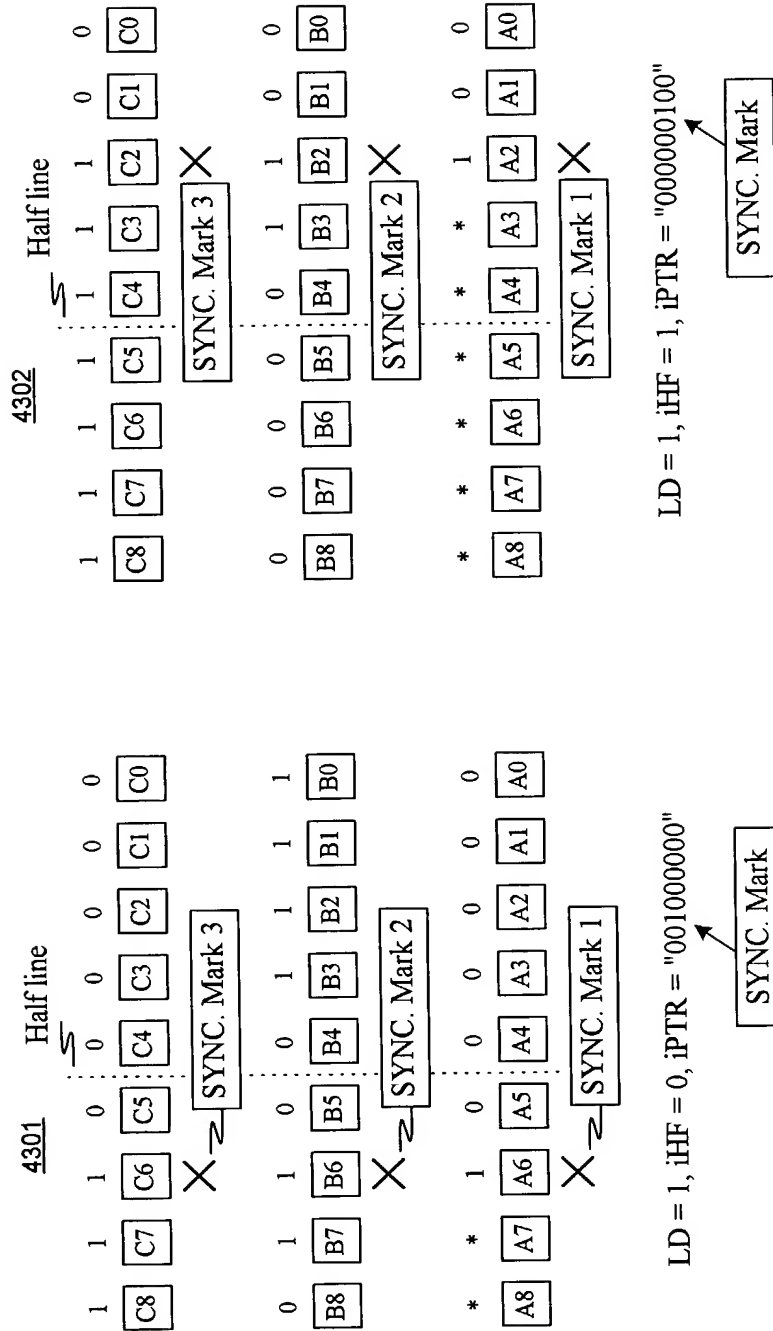
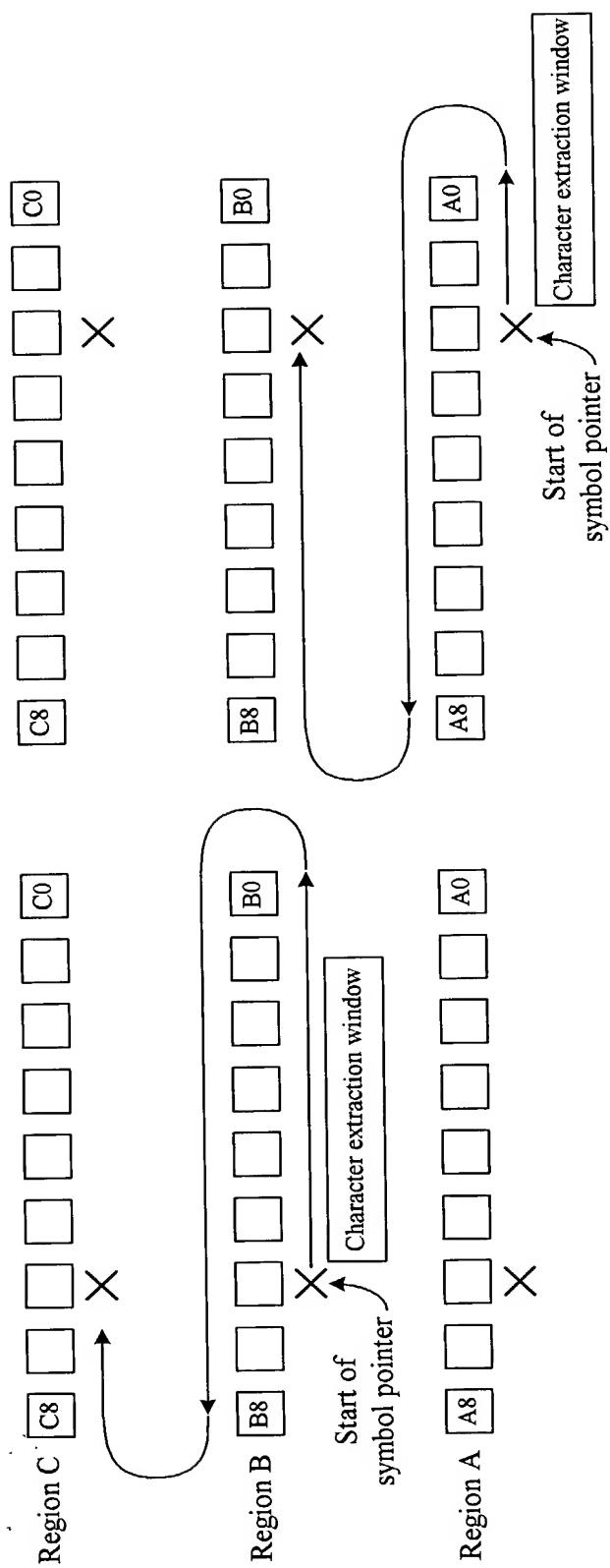


Fig. 43



LD = 1, iHF = 1, iPTR = "000000100"

LD = 1, iHF = 0, iPTR = "001000000"

Fig. 44

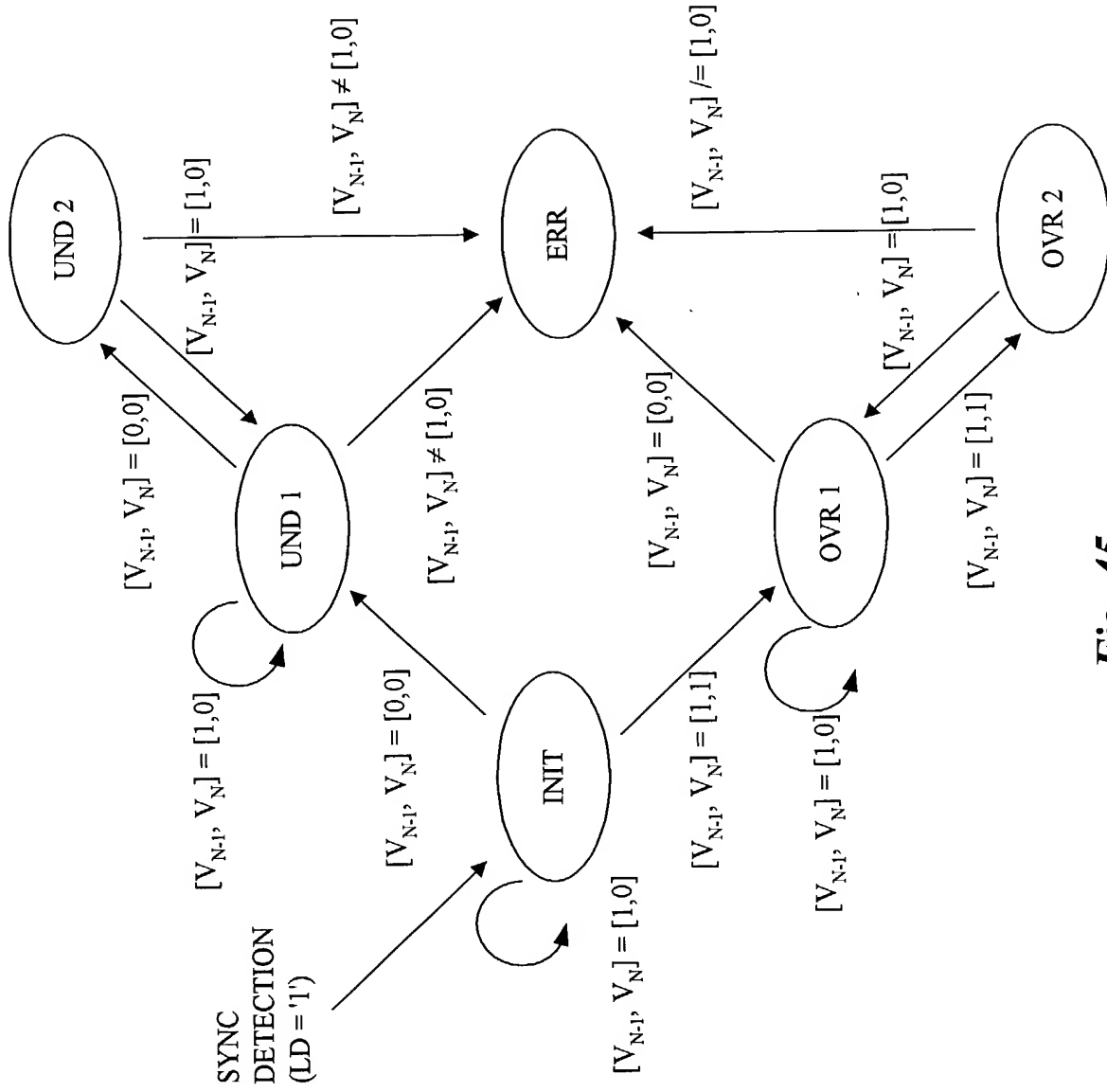


Fig. 45

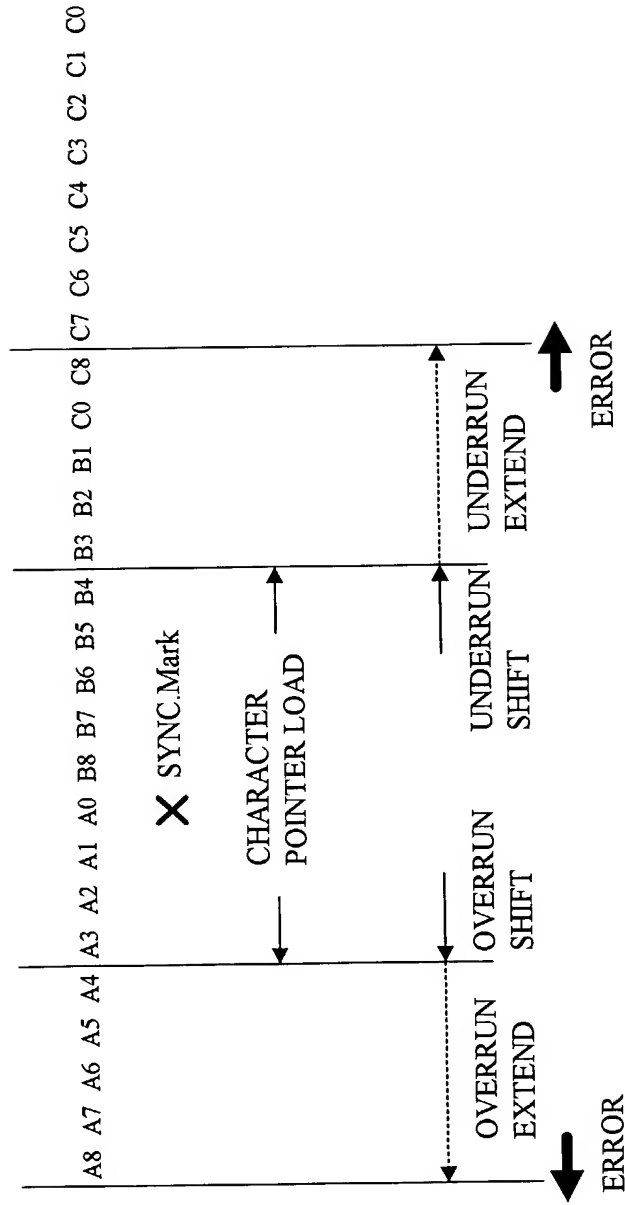


Fig. 46

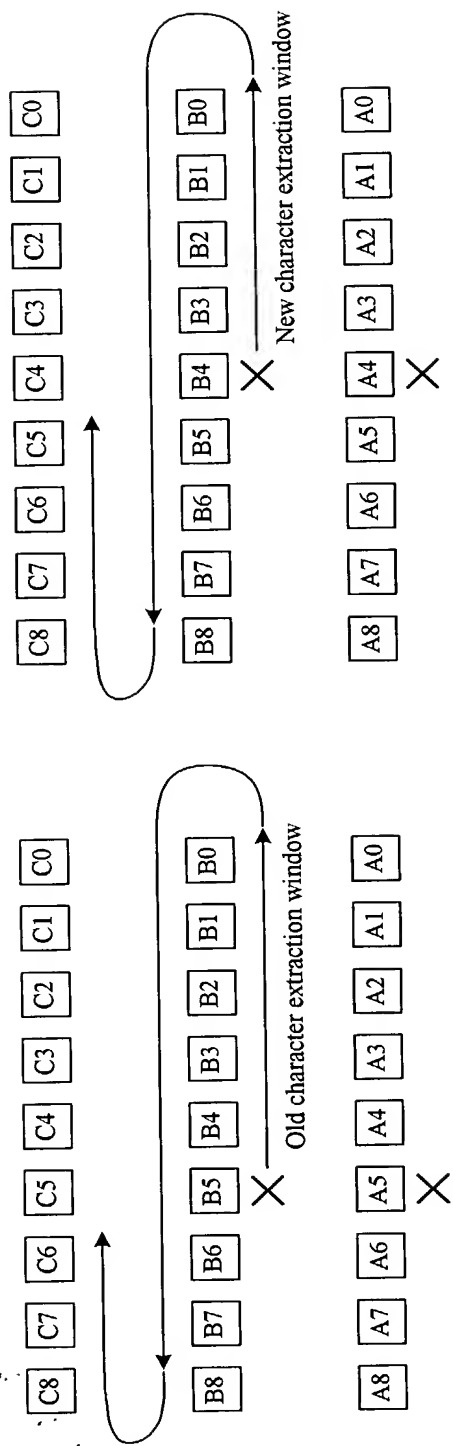


Fig. 47A

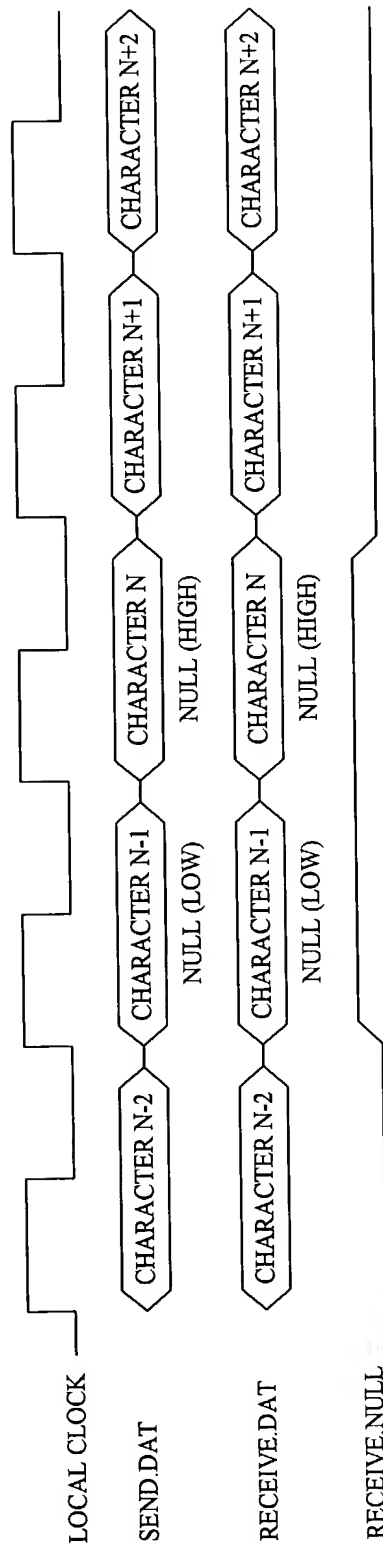


Fig. 47B

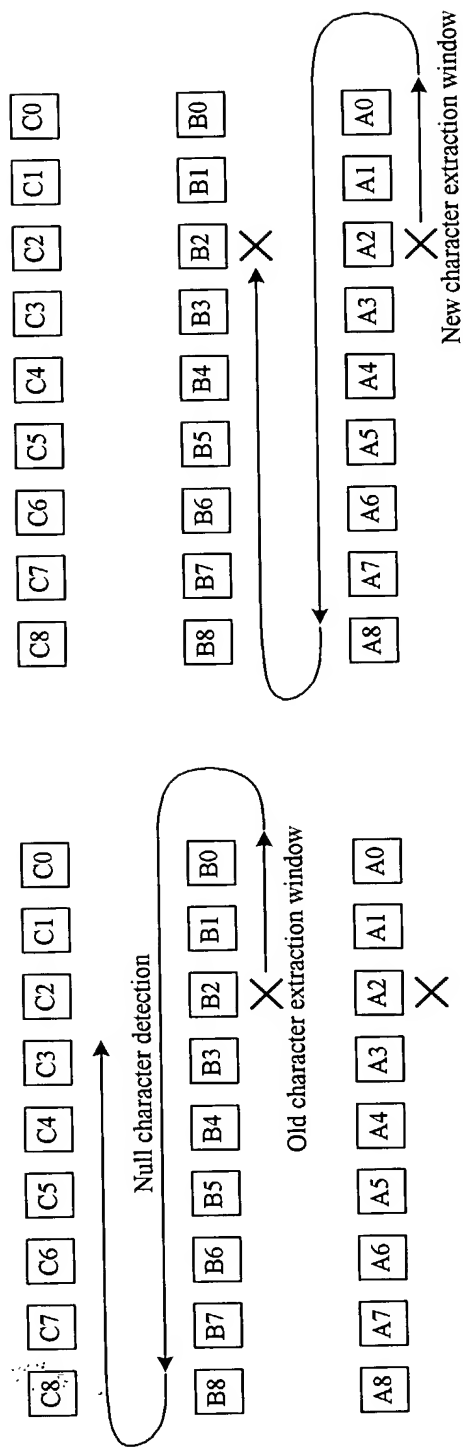


Fig. 48A

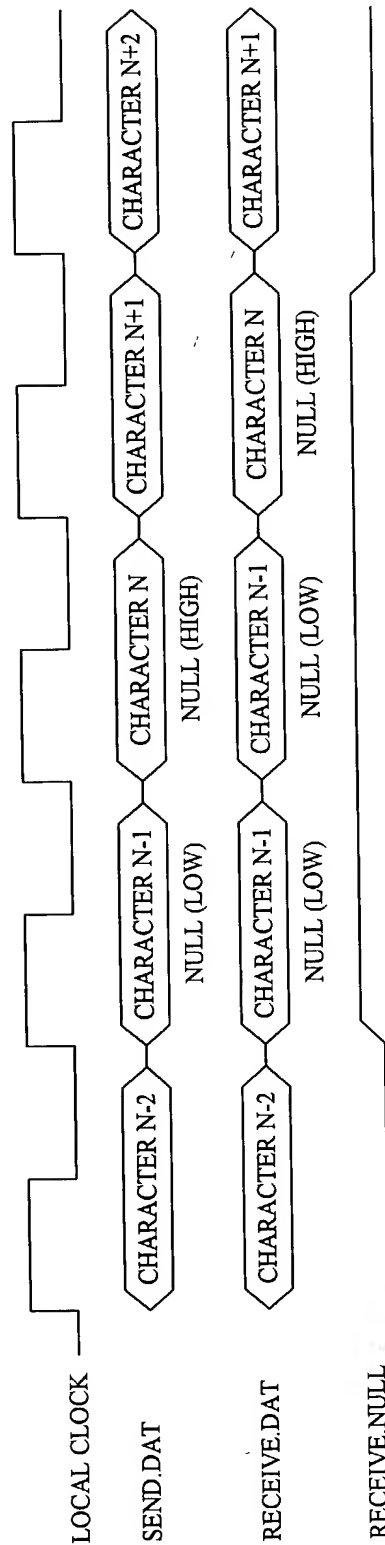


Fig. 48B

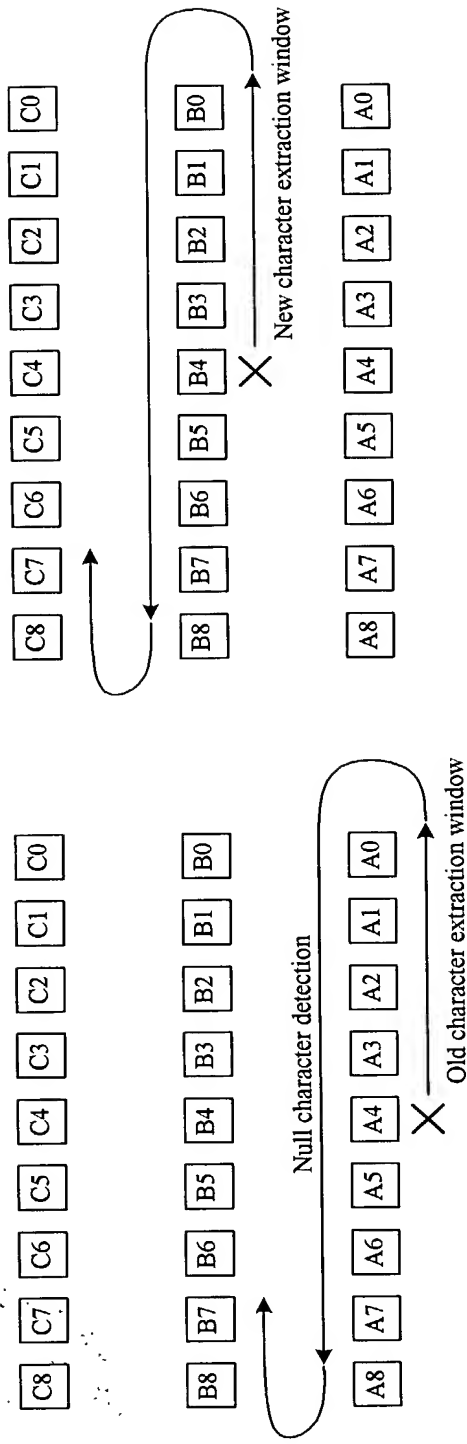


Fig. 49A

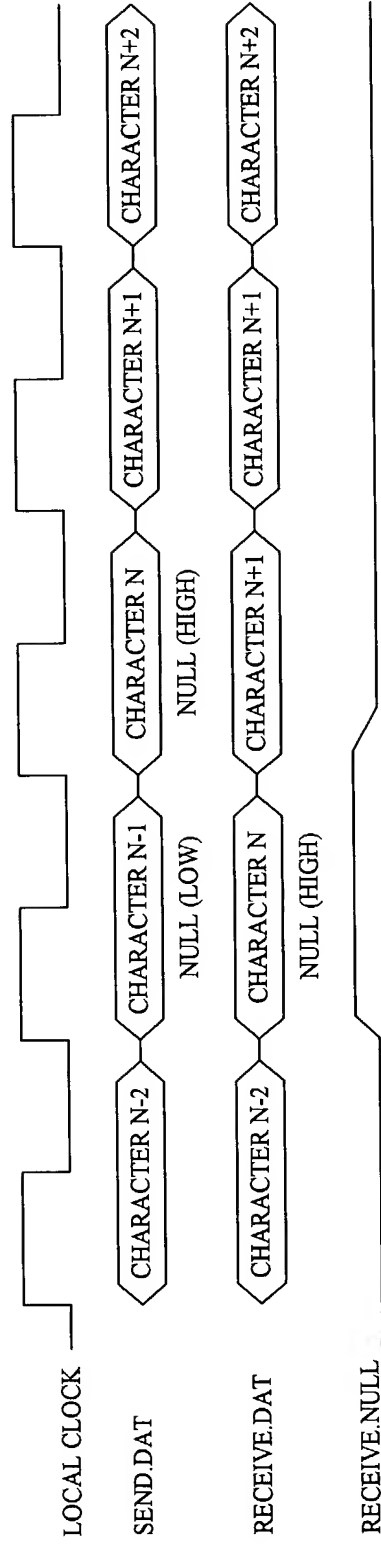


Fig. 49B